Contents Overview of Types	5 9
Chip Capacitors	11
General Technical Information	43
Quality Assurance	61
Measuring and Test Conditions Soldering Conditions	75 76
Taping, Packing and Weights	83
Subject Index Symbols and Terms	86 88

New lab assortments in film capacitors

Five at a stroke

To save you the trouble of inquiring for individual ratings to put into your design, there are now five practical sets of film capacitors:

- ► Lead spacing 5: 525 types, 50 to 400 V, 1 nF to 3.3 μF
- SilverCaps: the lowest-cost models, low in volume, 63 to 400 V, 1 nF to 10 μF
- MKPs in wound technology: for RF applications, 250 to 2000 V, 1.5 nF to 0.68 μF
- MKPs in stacked-film technology: 300 types, 160 to 1000 V, 1.5 nF to 1 μF
- ▶ Interference suppression: 150 types with a wide choice of ratings for different applications
 − X2 with small dimensions,
 Safe-X for maximum security against active flammability (X2) and Y for suppressing commonmode interference (Y2)



Tantalum Electrolytic Capacitors

Ferrites and inductors in modern office communications

The little things that do so much

In the multimedia age, ferrites and inductors often play a key role. In the switch-mode power supplies of PCs ETD cores ensure interference-free transmission of power. Ring and E cores in energy-saving lamps provide pleasant lighting. Interface transformers in ISDN systems satisfy the high demands of CCITT standards. And ultra-flat planar transformers supply units and installations with the necessary power.



For application-specific products and inductor design you can count on the support of our I.F.C. KNOW-HOW CENTER, right from the initial engineering phase.



Contents

Overvi	ew of Types	9
Chip C	Capacitors	11
Genera	al technical information	
1	Basic construction	43
2	Polarity	44
3	Standards	44
4 4.1 4.2 4.3 4.4 4.5 4.6 4.7 4.8	Voltages Rated voltage Maximum continuous voltage Operating voltage Surge voltage Polarity reversal voltage (incorrect polarity) Series back-to-back connection Inherent voltage Recharging Capacitance	45 45 45 46 46 47 47 47 47
5.1 5.2 5.3 5.4 5.5	Rated capacitance Capacitance tolerance Temperature dependence of the capacitance Frequency dependence of the capacitance Charge-discharge proof	48 48 48 49
6	Impedance / Equivalent series resistance (ESR)	50
7 7.1 7.2 8	AC power dissipation Superimposed alternating voltage for capacitors with solid electrolyte Maximum permissible ripple current and alternating voltage loads Dissipation factor	52 52 52 54
9 9.1 9.2 9.3 9.4	Leakage current Temperature and voltage dependence of the leakage current Time dependence of the leakage current Leakage current measurement Leakage current behavior after storage without applied voltage	55 55 56 56 57
10 10.1 10.2 10.3 10.4 10.5 10.6	Resistance to climatic stress Temperature range Minimum permissible operating temperature T_{\min} (Lower category temperature) Maximum permissible operating temperature T_{\max} (Upper category temperature) Damp heat conditions IEC climatic category Storage and transportation temperatures	57 57 57 57 57 58
11 11.1	Notes on mounting Cleaning agents	58 58

Contents

12	Standard barcode label	58
13	Packing	59
14	End of use and disposal	60
15	Structure of the ordering code (part number)	60
Quali	ty Assurance	61
1	General	61
1.1	Total quality management and zero defect concept	62
1.2	Quality assurance system	63
2	Quality assurance procedure	64
2.1 2.2	Material procurement	64 64
2.2	Product quality assurance Final inspection	64
2.4	Product monitoring	64
2.5	Manufacturing and quality assurance procedures for chip capacitors	65
3	Delivery quality	66
3.1	Random sampling Classification of incorretives / page conformancies	66
3.2 3.3	Classification of inoperatives / non-conformancies AQL figures	66 66
3.4	Incoming goods inspection	66
4	Service life	67
4.1	Failure criteria	68
5	Reliability	68
5.1	Failure rate (long-term failure rate)	68
5.2 5.3	Failure rate values Failure rate conversion factors	69 70
5.4	Effect of the circuit resistance (series resistance) on the failure rate	71
5.5	Example of how to calculate the failure rate	72
5.6	Failure rate for B 45 194	72
6	Supplementary information	73
7	Handling of claims and complaints	74
Meas	suring and Test Conditions	75
1	Test conditions selected from IEC 60-384-1	75
2	Tests with more stringent conditions for B 45 196-P	75
Solde	ering Conditions	76
1	Tests	76
2	Recommended solder pad layouts	77
3	Recommended soldering temperature profiles	78
4	Recommended soldering temperature profiles for B 45 194	80

Contents

Тарі	ing, Packing and Weights	83
1	Taping	83
2	Packing	84
3	Packing units and weights	84
Subj	ject Index	86
Sym	abols and Terms	88



Siemens filters from stock

Ready, steady, go

SCS has 100,000 SIFI filters in stock, ready to go as soon as your order arrives. We offer a big selection through all the many variants, ie



building-block system, different attenuation characteristics and packages, various kinds of leads and current ratings from 1 through 20 A.



Overview of Types

Туре	Series	"	Rated capacitance C_R	Features	Page
		Vdc	μF		

Chip capacit	ors				11
	B 45 194	4 20	0,10 3,3	Ultra-small design Case size Z	16
	B 45 196-E B 45 198-E	4 50	0,10 100	Standard version IECQ/CECC-approved	24
	B 45 196-H B 45 198-H	4 50	0,15 470	"HighCap" Very high volumetric efficiency	27
KTA0145-4	B 45 196-P B 45 198-P	4 50	0,10 150	"Performance" Extremely high reliability, IECQ/CECC-approved (150 °C version)	31
	B 45 197 B 45 198-R	6,3 50	3,3 330	"SpeedPower" Low ESR, for power supplies with very high clock frequencies	35

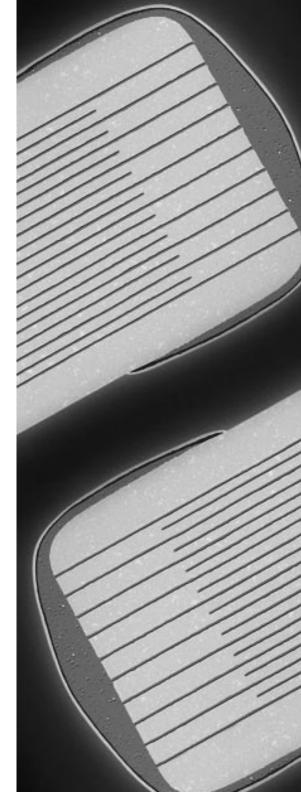


European technology center for ceramic components

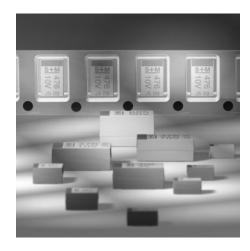
There when you need us

This is an organization that's proven its worth. Because it stands for more customer proximity and thus better service. Here you get information straight from the source, implementation of the latest technologies and products that match the market. Concentration of resources means that design engineers and production engineers are working side by side. And SCS warehousing directly at the plant ensures fastest possible delivery.





Chip Capacitors



	Page
B 45 194	
General specifications	12
Overview of available types	15
Technical data and ordering codes	16
Characteristic curves	17
B 45 196, B 45 197, B 45 198	
General specifications	18
Overview of available types	22
Technical data and ordering codes	24
Characteristic curves	37



Construction

- Polar tantalum capacitors with solid electrolyte
- Flame-retardant plastic case (UL 94 V-0)
- Tinned terminals

Features

- Ultra-small design
 Case size Z = 0805 and P = 1206 (low profile)
- High volumetric efficiency
- Excellent solderability
- Stable temperature and frequency characteristics
- Low leakage current, low dissipation factor
- Low self-inductance
- High resistance to shock and vibration
- Suitable for use without series resistor

Applications

- Telecommunications (e.g. pagers, handies)
- Data processing (e.g. PCMA cards)
- Medical engineering (e.g. hearing aids)

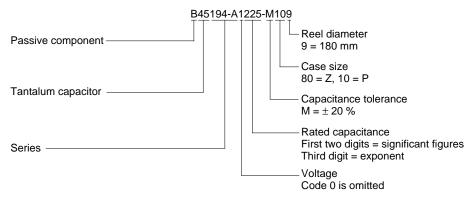
Soldering

Suitable for reflow soldering (IR and vapor phase) and wave soldering

Delivery mode

Taped and reeled in accordance with IEC 286-3

Ordering code structure





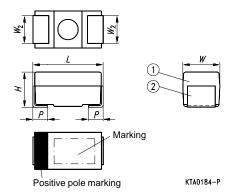


Specifications and characteristics in brief

Series	B 45 194
Overview of available types	Page 15
Rated voltage V_R (up to 85 °C)	4 20 Vdc
Rated capacitance C_R	0,10 3,3 μF
Tolerance	± 20 %
Failure rate	Refer to page 72
Leakage current (V _R , 2 min, 20 °C)	≤ 0,5 μA
IEC climatic category	in accordance with IEC 68-1 55/125/21 (–55/+125 °C; 21 days damp heat test)

Any general information given in the second part of this data book (starting with page 43) only applies to B 45 194 if the B 45 194 series or its cases Z and P are explicitly mentioned. Please contact your nearest Siemens Office for Passive Components if you need further information.

Dimensional drawing



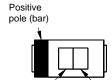
- ① Encapsulation: molded epoxy resin
- ② NiFe; surface Sn60/Pb40 Slotted anode terminal for case size P

Case size	Dimensions in n	nm (inches)			
	L ± 0,2 ± (,008)	W ± 0,2 ± (,008)	H max.	W ₂ ± 0,1 ± (,004)	p
Z	2,0	1,25	1,2	0,9	0,5 ± 0,2
	(,079)	(,049)	(,047)	(,035)	(,020 ± ,008)
Р	3,2	1,6	1,2	1,2	0,8 ± 0,3
	(,126)	(,062)	(,047)	(,047)	(,031 ± ,012)



Marking

Case size Z



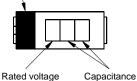
Rated voltage (in coded form)

Capacitance (in coded form) KTA0205-G

Case size P



(in coded form)



(in coded form) KTA0206-P

Voltage coding

Rated voltage	4	6,3	10	16	20
Code letter	G	J	Α	С	D

Capacitance coding

1st digit (capac	itance	e)						
Code letter A E J N S W								
Cap. value	1	1,5	2,2	3,3	4,71)	6,81)		
2nd digit (multiplier)								
Code number	5			6				
Multiplier	10 ⁵			10 ⁶				

Example: 6,3 V; 1,5 µF in P case: JE6

Voltage: 6.3 V = J

Capacitance: $1.5 \mu F = 1.5 \cdot 10^6 pF$

1,5 = E

 $10^6 = 6$

For case size Z the code number for the multiplier

is omitted. Example: JE

¹⁾ Cap. value 0,47 and 0,68 µF for case size Z



Overview of available types

V _R (Vdc) up to 85 °C	4,0		6,3		10		16		20	
<i>C</i> _R (μF)			•							
0,10									P)
0,15									P)
0,22									Р)
0,33									Р)
0,47								Z	Р)
0,68							Z	Р	Р)
1,0					Z	Р		P		
1,5			Z	Р	F)				
2,2	Z	Р		P						
3,3	1	P								
Features	Ultra-si Low pro	mall des ofile	ign						•	



For characteristic curves see page 17

$V_{\rm R}$ 1) up to 85 °C (up to 125 °C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 2 min)	Ordering code
Vdc	μF			μΑ	
4,0	2,2	Z	0,10	0,5	B45194-A225-M809
(2,5)	2,2	P	0,04	0,5	B45194-A225-M109
	3,3	P	0,04	0,5	B45195-A335-M109
6,3	1,5	Z	0,10	0,5	B45194-A1155-M809
(4)	1,5	P	0,04	0,5	B45194-A1155-M109
	2,2	P	0,04	0,5	B45194-A1225-M109
10	1,0	Z	0,10	0,5	B45194-A2105-M809
(6,3)	1,0	P	0,04	0,5	B45194-A2105-M109
	1,5	P	0,04	0,5	B45194-A2155-M109
16	0,47	Z	0,10	0,5	B45194-A3474-M809
(10)	0,68	Z	0,10	0,5	B45194-A3684-M809
	0,68	P	0,04	0,5	B45194-A3684-M109
	1,0	P	0,04	0,5	B45194-A3105-M109
20	0,10	Р	0,04	0,5	B45194-A4104-M109
(13)	0,15	P	0,04	0,5	B45194-A4154-M109
	0,22	P	0,04	0,5	B45194-A4224-M109
	0,33	P	0,04	0,5	B45194-A4334-M109
	0,47	P	0,04	0,5	B45194-A4474-M109
	0,68	Р	0,04	0,5	B45194-A4684-M109

Capacitance tolerance: $M = \pm 20 \%$

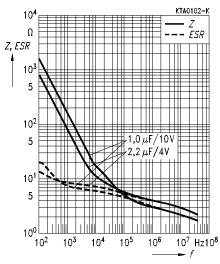
¹⁾ Surge voltage $V_S = 1.3 \cdot V_R$



Impedance Z and equivalent series resistance ESR versus frequency f

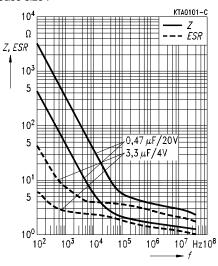
Typical behavior

Case size Z



Impedance Z and equivalent series resistance ESR versus frequency f Typical behavior

Case size P



Construction

- Polar tantalum capacitors with solid electrolyte
- Flame-retardant plastic case (UL 94 V-0)
- Optionally tinned or gold-plated terminals (gold-plated terminals for case size A upon request)



Features

- High volumetric efficiency
- Excellent solderability
- Stable temperature and frequency characteristics
- Low leakage current, low dissipation factor
- Low self-inductance
- High resistance to shock and vibration
- Suitable for use without series resistor

Applications

- Telecommunications (e.g. mobile phones, private branch exchanges)
- Data processing (e.g. laptops, main frames)
- Measuring and control engineering
- Automotive electronics
- Medical engineering
- Switch-mode power supplies with very high clock frequencies (300 kHz)
- DC/DC converters

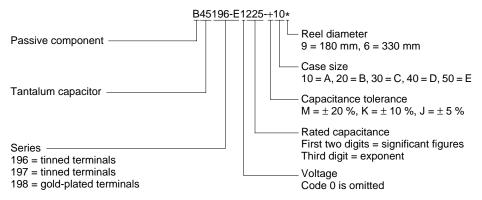
Soldering

Suitable for reflow soldering (IR and vapor phase) and wave soldering

Delivery mode

Taped and reeled in accordance with IEC 286-3

Ordering code structure





Specifications and characteristics in brief

Series	B 45 196-E Standard	B 45 196-H HighCap	B 45 196-P Performance	B 45 197 SpeedPower (Low ESR)
Overview of available types	Page 22		Page 23	
Rated voltage V_R (up to 85 °C)	4 50 Vdc	4 50 Vdc	4 50 Vdc	6,3 50 Vdc
Rated capacitance C_{R}	0,10 100 μF	0,15 470 μF	0,10 150 μF	3,3 330 μF
Tolerance	± 10 %, ± 20 % ± 5% (on request)	± 10 %, ± 20 % ± 5% (on request)	± 10 %, ± 20 % ± 5% (on request)	± 10 %, ± 20 % ± 5% (on request)
Failure rate	at 40 °C; ≤ <i>V</i> _R , <i>R</i> _S ≥	$\geq 3\Omega/V$ (1 fit = 1 · 10	⁻⁹ failures/h)	
$C_{R} \cdot V_{R} \le 330 \mu F \cdot V$ $C_{R} \cdot V_{R} > 330 \mu F \cdot V$		≤ 8 fit ≤ 24 fit	≤ 0,8 fit ≤ 2,5 fit	≤ 8 fit ≤ 12 fit
Service life	> 500 000 h	> 500 000 h	> 500 000 h	> 500 000 h
Leakage current (V _R , 5 min, 20 °C)	10 nA/μC	10 nA/μC	10 nA/μC	10 nA/μC
ESR	_	_	_	100 600 mΩ
Detail specification (tinned terminals)	IEC-QC300801/ US0001 CECC30801-801	CECC30801-802	IEC-QC300801/ US0001 CECC30801-801	CECC30801-805
Quality approval	IECQ CECC		IECQ CECC	
IEC climatic category	in accordance with 55/125/56 (-55/+12	IEC 68-1 25 °C; 56 days dam	o heat test)	

For types B 45 196-P, individual tests are carried out under more extreme conditions, supplementary to the tests specified by CECC.

Examples:

Damp heat 85 (+2) $^{\circ}$ C, 85 ... 90% relative humidity Rapid temperature change 100 cycles, -55° C/+ 125 $^{\circ}$ C, 30 min.

Surge voltage 10⁴ charge cycles

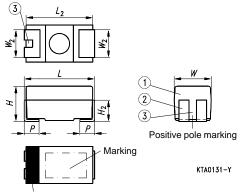
Impulse test 10⁶ cycles

Types B 45 196-P can be operated at temperatures up to 150 °C. Details for this operating condition must be agreed upon between supplier and customer.



B 45 196, B 45 197 B 45 198

Dimensional drawing



Positive pole marking

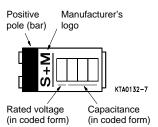
- ① Encapsulation: molded epoxy resin
- ② NiFe; surface Sn60/Pb40 or Sn90/Pb10 or gold-plated
- 3 Reduced slot length for case size A

Case size	Dimensions	s in mm (incl	nes)				
	L	W	Н	L ₂ typ.	$W_2 \pm 0.1$ $\pm (.004)$	H ₂ typ.	p ± 0,3 ±(,012)
A	3,2 ± 0,2	1,6 ± 0,2	1,6 ± 0,2	3,0	1,2	1,0	0,8
	(,126±,008)	(,063±,008)	(,063±,008)	(,118)	(,047)	(,039)	(,031)
В	3,5 ± 0,2	2,8 ± 0,2	1,9 ± 0,2	3,3	2,2	1,2	0,8
	(,138±,008)	(,110±,008)	(,075±,008)	(,130)	(,087)	(,047)	(,031)
С	6,0 ± 0,3	3,2 ± 0,3	2,5 ± 0,3	5,8	2,2	1,5	1,3
	(,236±,012)	(,126±,012)	(,098±,012)	(,228)	(,087)	(,059)	(,051)
D	7,3 ± 0,3	4,3 ± 0,3	2,8 ± 0,3	7,1	2,4	1,6	1,3
	(,287±,012)	(,169±,012)	(,110±,012)	(,280)	(,094)	(,062)	(,051)
E	7,3 ± 0,3	4,3 ± 0,3	4,1 ± 0,3	7,1	2,4	1,6	1,3
	(,287±,012)	(,169±,012)	(,157±,012)	(,280)	(,094)	(,062)	(,051)

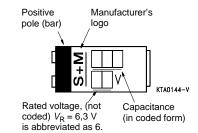


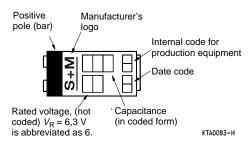
Marking

Case size A



Case size B





Case sizes C, D, E

Voltage coding for case size A

Rated voltage	4	6,3	10	16	20	25	35	50
Code letter	G	J	Α	С	D	Е	٧	Т

Capacitance coding

1st and 2nd digit	Capacitan	ce in pF
3rd digit		$4 = 10^4 \text{ pF}$ $5 = 10^5 \text{ pF}$ $6 = 10^6 \text{ pF}$ $7 = 10^7 \text{ pF}$

Date coding

Year	Month	
H = 1996	1 = January	7 = July
J = 1997	2 = February	8 = August
K = 1998	3 = March	9 = September
L = 1999	4 = April	O = October
M = 2000	5 = May	N = November
	6 = June	D = December

In addition to the year and month of manufacture, the stamp includes another two figures which internally allow us an assignment to concrete production equipment. Stamping of chips in case sizes A and B with date code is currently not possible for reasons of space.



B 45 196, B 45 197 B 45 198

Overview of available types

Series	B 45	B 45 196-E, tinned terminals B 45 198-E, gold-plated terminals ¹⁾ Standard						B 45		H, go		termir ated t	nals ermin	ıals ¹⁾		
Page	24								27							
V _R (Vdc) up to 85 °C	4	6,3	10	16	20	25	35	50	4	6,3	10	16	20	25	35	50
<u>C</u> _R (μF)																1
0,10							Α	Α								
0,15							Α	В								Α
0,22							Α	В								Α
0,33							Α	В								
0,47						Α	В	С							Α	В
0,68					Α	Α	В	С							Α	
1,0				Α	Α		В	С						Α	Α	
1,5			Α	Α		В	С	D					Α	Α	В	С
2,2		Α	Α		В	В	С	D				Α	Α		В	
3,3	Α	Α		В	В	С	С	D			Α	Α	Α	В		
4,7	Α		В	В	С	С	D	D		Α	Α	Α	ВА	В	С	
6,8		В	В	С	С	D	D		Α	Α	Α	ВА	В	С	С	Е
10	В	В	С	С		D	D		Α	Α	АВ	В	СВ	С		Е
15	В	С	С		D	D			Α	АВ	В	СВ	С		D	
22	С	С		D	D				ВА	В	ВС	С	С	D	Е	
33	С		D	D					В	ВС	С	С	D	E D	Е	
47		D	D						ВС	ВС	С	D	DE	Е		
68	D	D							С	С	DC	D	Е			
100	D								С	CD	D	DE	Е			
150									D	D	DE	Е				
220									D	DE	E D					
330									E	ED	E					
470									E	Е						
Features		ndard CEC				CQ				iculai metri			/ .			



¹⁾ Gold-plated terminals available for case sizes B, C, D, E (A upon request), currently without CECC approval

²⁾ Additional ratings upon request



Overview of available types

Series	B 45	B 45 198-P, gold-plated terminals ¹⁾								197- <i>A</i> 198-F edPov	R, gold	d-plate	d term)
Page	31								35						
V _R (Vdc) up to 85 °C	4	6,3	10	16	20	25	35	50	6,3	10	16	20	25	35	50
C _R (μF)														'	
0,10							Α	Α							
0,15							Α	В							
0,22							Α	В							
0,33							Α	В							
0,47						Α	В	С							
0,68					Α	Α	В	С							
1,0				Α	Α	Α	В	С							
1,5			Α	Α	Α	В	С	D							
2,2		Α	Α	Α	В	В	С	D							
3,3	Α	Α	Α	В	В	С	С	D						С	
4,7	Α	Α	В	В	С	С	D	D					С	D	D
6,8	Α	В	В	С	С	С	D					С		DE	Е
10	В	В	вс	С	С	CD	D				С	С		DE	
15	В	С	С	С	D	D				С	С		D	DE	
22	С	С	С	C D	D	D			С	С		D	DE	E	
33	С	С	D	D	D				С		D	DE	Е		
47	С	CD	D	D						D	D	Е			
68	D	D	D						D	D	Е	Е			
100	D	D	D						D	DE	Е				
150	D								Е	E					
220									Е	Е					
330									Е	Е					
Features	e. g. for automotive electronics and medical applications,							Low ESR, for switch-mode power supplies with very high clock frequencies (e. g. telecom applications).							

¹⁾ Gold-plated terminals available for case sizes B, C, D, E (A upon request), currently without CECC approval

²⁾ Additional ratings upon request





For characteristic curves see page 37/38 ff

$V_{\rm R}$ up to 85°C (up to 125°C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned terminals
4	3,3	Α	0,06	0,5	9,0	B45196-E335-+10*
(2,5)	4,7	Α	0,06	0,5	7,0	B45196-E475-+10*
	10	В	0,06	0,5	4,5	B45196-E106-+20*
	15	В	0,06	0,6	3,5	B45196-E156-+20*
	22	С	0,06	0,9	2,4	B45196-E226-+30*
	33	С	0,06	1,3	2,0	B45196-E336-+30*
	68	D	0,06	2,7	1,1	B45196-E686-+40*
	100	D	0,08	4,0	0,8	B45196-E107-+40*
6,3	2,2	Α	0,06	0,5	10	B45196-E1225-+10*
(4)	3,3	Α	0,06	0,5	7,0	B45196-E1335-+10*
	6,8	В	0,06	0,5	4,5	B45196-E1685-+20*
	10	В	0,06	0,6	3,5	B45196-E1106-+20*
	15	С	0,06	1,0	2,4	B45196-E1156-+30*
	22	С	0,06	1,4	2,0	B45196-E1226-+30*
	47	D	0,06	3,0	1,1	B45196-E1476-+40*
	68	D	0,06	4,3	0,8	B45196-E1686-+40*
10	1,5	Α	0,06	0,5	10	B45196-E2155-+10*
(6,3)	2,2	Α	0,06	0,5	7,0	B45196-E2225-+10*
	4,7	В	0,06	0,5	4,5	B45196-E2475-+20*
	6,8	В	0,06	0,7	3,5	B45196-E2685-+20*
	10	С	0,06	1,0	2,4	B45196-E2106-+30*
	15	С	0,06	1,5	2,0	B45196-E2156-+30*
	33	D	0,06	3,3	1,1	B45196-E2336-+40*
	47	D	0,06	4,7	0,8	B45196-E2476-+40*
16	1,0	Α	0,04	0,5	10	B45196-E3105-+10*
(10)	1,5	Α	0,06	0,5	8,0	B45196-E3155-+10*
	3,3	В	0,06	0,6	5,0	B45196-E3335-+20*
	4,7	В	0,06	0,8	3,5	B45196-E3475-+20*
	6,8	С	0,06	1,1	2,4	B45196-E3685-+30*
	10	С	0,06	1,6	2,0	B45196-E3106-+30*
	22	D	0,06	3,6	1,1	B45196-E3226-+40*
	33	D	0,06	5,3	1,0	B45196-E3336-+40*

¹⁾ Replace 196 by 198 for gold-plated terminals + Insert code letter for required capacitance tolerance: M = ± 20 %, K = ± 10 % (J = ± 5 % upon request)

^{*} Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm





V _R up to 85°C	C_{R}	Case size	$\tan \delta_{\text{max}}$ (20°C,	I _{lk, max} (20 °C, V _R ,	Z _{max} (20°C,	Ordering code 1)
(up to 125°C)			120 Hz)	5 min)	100 kHz)	
Vdc	μF			μΑ	Ω	Tinned terminals
20	0,68	А	0,04	0,5	12	B45196-E4684-+10*
(13)	1,0	Α	0,04	0,5	9,0	B45196-E4105-+10*
	2,2	В	0,06	0,5	6,0	B45196-E4225-+20*
	3,3	В	0,06	0,7	4,5	B45196-E4335-+20*
	4,7	С	0,06	1,0	2,4	B45196-E4475-+30*
	6,8	С	0,06	1,4	2,0	B45196-E4685-+30*
	15	D	0,06	3,0	1,2	B45196-E4156-+40*
	22	D	0,06	4,4	1,0	B45196-E4226-+40*
25	0,47	Α	0,04	0,5	13	B45196-E5474-+10*
(16)	0,68	Α	0,04	0,5	10	B45196-E5684-+10*
	1,5	В	0,06	0,5	7,0	B45196-E5155-+20*
	2,2	В	0,06	0,6	5,0	B45196-E5225-+20*
	3,3	С	0,06	0,9	2,8	B45196-E5335-+30*
	4,7	С	0,06	1,2	2,3	B45196-E5475-+30*
	6,8	D	0,06	1,7	1,8	B45196-E5685-+40*
	10	D	0,06	2,5	1,2	B45196-E5106-+40*
	15	D	0,06	3,8	1,0	B45196-E5156-+40*
35	0,10	А	0,04	0,5	28	B45196-E6104-+10*
(23)	0,15	Α	0,04	0,5	23	B45196-E6154-+10*
	0,22	Α	0,04	0,5	19	B45196-E6224-+10*
	0,33	Α	0,04	0,5	15	B45196-E6334-+10*
	0,47	В	0,04	0,5	11	B45196-E6474-+20*
	0,68	В	0,04	0,5	8,0	B45196-E6684-+20*
	1,0	В	0,04	0,5	7,0	B45196-E6105-+20*
	1,5	С	0,06	0,6	4,8	B45196-E6155-+30*
	2,2	С	0,06	0,8	3,2	B45196-E6225-+30*
	3,3	С	0,06	1,2	2,4	B45196-E6335-+30*
	4,7	D	0,06	1,7	1,5	B45196-E6475-+40*
	6,8	D	0,06	2,4	1,2	B45196-E6685-+40*
	10	D	0,06	3,5	1,0	B45196-E6106-+40*





V _R up to 85°C (up to 125°C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned terminals
50	0,10	Α	0,04	0,5	27	B45196-E7104-+10*
(33)	0,15	В	0,04	0,5	22	B45196-E7154-+20*
	0,22	В	0,04	0,5	18	B45196-E7224-+20*
	0,33	В	0,04	0,5	14	B45196-E7334-+20*
	0,47	С	0,04	0,5	7,2	B45196-E7474-+30*
	0,68	С	0,04	0,5	6,4	B45196-E7684-+30*
	1,0	С	0,04	0,5	4,8	B45196-E7105-+30*
	1,5	D	0,06	0,8	4,0	B45196-E7155-+40*
	2,2	D	0,06	1,1	2,8	B45196-E7225-+40*
	3,3	D	0,06	1,7	1,6	B45196-E7335-+40*
	4,7	D	0,06	2,4	1,2	B45196-E7475-+40*



For characteristic curves see page 37/38 ff

V _R up to 85°C (up to 125°C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{Ik, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned terminals
4	6,8	Α	0,06	0,5	6,0	B45196-H685-+10*
(2,5)	10	Α	0,06	0,5	4,5	B45196-H106-+10*
	15	Α	0,06	0,6	4,0	B45196-H156-+10*
	22 ²⁾	Α	0,06	0,9	3,5	B45196-H226-+10*
	22	В	0,06	0,9	3,0	B45196-H226-+20*
	33	В	0,06	1,3	2,5	B45196-H336-+20*
	47	В	0,06	1,9	2,3	B45196-H476-+20*
	47	С	0,06	1,9	1,6	B45196-H476-+30*
	68	С	0,06	2,7	1,5	B45196-H686-+30*
	100	С	0,08	4,0	1,4	B45196-H107-+30*
	150	D	0,08	6,0	0,8	B45196-H157-+40*
	220	D	0,08	8,8	0,8	B45196-H227-+40*
	330	E	0,08	13,2	0,8	B45196-H337-+50*
	470	E	0,08	18,8	0,6	B45196-H477-+50*
6,3	4,7	Α	0,06	0,5	5,5	B45196-H1475-+10*
(4)	6,8	Α	0,06	0,5	4,5	B45196-H1685-+10*
	10	Α	0,06	0,6	4,0	B45196-H1106-+10*
	15	Α	0,06	0,9	3,8	B45196-H1156-+10*
	15	В	0,06	0,9	3,0	B45196-H1156-+20*
	22	В	0,06	1,4	2,5	B45196-H1226-+20*
	33	В	0,06	2,1	2,2	B45196-H1336-+20*
	33	С	0,06	2,1	1,6	B45196-H1336-+30*
	47	В	0,06	3,0	2,0	B45196-H1476-+20*
	47	С	0,06	3,0	1,5	B45196-H1476-+30*
	68	С	0,06	4,3	1,4	B45196-H1686-+30*
	100	С	0,08	6,3	1,2	B45196-H1107-+30*
	100	D	0,08	6,3	0,8	B45196-H1107-+40*
	150	D	0,08	9,5	0,8	B45196-H1157-+40*
	220	D	0,08	13,9	0,8	B45196-H1227-+40*
	220	E	0,08	13,9	0,8	B45196-H1227-+50*
	330	D	0,08	20,8	0,8	B45196-H1337-+40*
	330	E	0,08	20,8	0,6	B45196-H1337-+50*
	470	E	0,08	29,6	0,6	B45196-H1477-+50*

¹⁾ Replace 196 by 198 for gold-plated terminals

⁺ Insert code letter for required capacitance tolerance: M = \pm 20 %, K = \pm 10 % (J = \pm 5 % upon request) * Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm

²⁾ Upon request



$V_{\rm R}$ up to 85 °C (up to 125 °C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF		0 ,	μΑ	Ω	Tinned terminals
10	3,3	A	0,06	0,5	5,5	B45196-H2335-+10*
(6,3)	4,7	A	0,06	0,5	4,5	B45196-H2475-+10*
(, ,	6,8	A	0,06	0,7	4,0	B45196-H2685-+10*
	10	A	0,06	1,0	3,8	B45196-H2106-+10*
	10	В	0.06	1,0	3,0	B45196-H2106-+20*
	15	В	0,06	1,5	2,5	B45196-H2156-+20*
	22	В	0,06	2,2	2,3	B45196-H2226-+20*
	22	С	0,06	2,2	1,6	B45196-H2226-+30*
	33	С	0,06	3.0	1,5	B45196-H2336-+30*
	47	C	0,06	4,7	1,4	B45196-H2476-+30*
	682)	С	0,06	6,8	1,2	B45196-H2686-+30*
	68	D	0,06	6,8	0,8	B45196-H2686-+40*
	100	D	0,08	10	0,8	B45196-H2107-+40*
	150	D	0,08	15	0,8	B45196-H2157-+40*
	150	E	0,08	15	0,8	B45196-H2157-+50*
	2202)	D	0,08	22	0,8	B45196-H2227-+40*
	220	E	0,08	22	0,6	B45196-H2227-+50*
	330	E	0,08	33	0,6	B45196-H2337-+50*
16	2,2	Α	0,06	0,5	6,5	B45196-H3225-+10*
(10)	3,3	Α	0,06	0,5	5,0	B45196-H3335-+10*
	4,7	Α	0,06	0,8	4,0	B45196-H3475-+10*
	6,82)	Α	0,06	1,1	3,8	B45196-H3685-+10*
	6,8	В	0,06	1,1	3,0	B45196-H3685-+20*
	10	В	0,06	1,6	2,5	B45196-H3106-+20*
	15 ²⁾	В	0,06	2,4	2,3	B45196-H3156-+20*
	15	С	0,06	2,4	1,6	B45196-H3156-+30*
	22	С	0,06	3,5	1,5	B45196-H3226-+30*
	33	С	0,06	5,3	1,4	B45196-H3336-+30*
	47	D	0,06	7,5	0,8	B45196-H3476-+40*
	68	D	0,06	10,9	0,8	B45196-H3686-+40*
	1002)	D	0,08	16	0,8	B45196-H3107-+40*
	100	E	0,08	16	0,8	B45196-H3107-+50*
	150 ²⁾	E	0,08	24	0,6	B45196-H3157-+50*

¹⁾ Replace 196 by 198 for gold-plated terminals

^{*} Insert code letter for required capacitance tolerance: $M = \pm 20$ %, $K = \pm 10$ % ($J = \pm 5$ % upon request) * Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm

²⁾ Upon request



V _R up to 85°C (up to 125°C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	<i>I</i> _{lk, max} (20 °C, <i>V</i> _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned terminals
20	1,5	Α	0,06	0,5	8,0	B45196-H4155-+10*
(13)	2,2	Α	0,06	0,5	6,0	B45196-H4225-+10*
	3,32)	Α	0,06	0,7	4,0	B45196-H4335-+10*
	4,72)	Α	0,06	0,9	3,5	B45196-H4475-+10*
	4,7	В	0,06	0,9	3,0	B45196-H4475-+20*
	6,8	В	0,06	1,4	2,5	B45196-H4685-+20*
	102)	В	0,06	2,0	2,3	B45196-H4106-+20*
	10	С	0,06	2,0	1,6	B45196-H4106-+30*
	15	С	0,06	3,0	1,5	B45196-H4156-+30*
	22	С	0,06	4,4	1,4	B45196-H4226-+30*
	33	D	0,06	6,6	0,8	B45196-H4336-+40*
	47	D	0,06	9,4	0,8	B45196-H4476-+40*
	47	E	0,06	9,4	0,8	B45196-H4476-+50*
	68	E	0,06	13,6	0,8	B45196-H4686-+50*
	100 ²⁾	E	0,08	20,0	0,8	B45196-H4107-+50*
25	1,0	Α	0,04	0,5	8,0	B45196-H5105-+10*
(16)	1,5	Α	0,06	0,5	7,0	B45196-H5155-+10*
	3,3	В	0,06	0,8	4,0	B45196-H5335-+20*
	4,7	В	0,06	1,2	3,2	B45196-H5475-+20*
	6,8	С	0,06	1,7	2,0	B45196-H5685-+30*
	10	С	0,06	2,5	1,6	B45196-H5106-+30*
	22	D	0,06	5,5	0,8	B45196-H5226-+40*
	332)	D	0,06	8,3	0,8	B45196-H5336-+40*
	33	E	0,06	8,3	0,8	B45196-H5336-+50*
	472)	E	0,06	11,7	0,8	B45196-H5476-+50*

²⁾ Upon request



V _R up to 85°C (up to 125°C)	C _R	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned terminals
35	0,47	Α	0,04	0,5	11	B45196-H6474-+10*
(23)	0,68	Α	0,04	0,5	8,0	B45196-H6684-+10*
	1,0	Α	0,04	0,5	7,0	B45196-H6105-+10*
	1,5	В	0,06	0,5	6,0	B45196-H6155-+20*
	2,2	В	0,06	0,8	4,0	B45196-H6225-+20*
	4,7	С	0,06	1,6	2,0	B45196-H6475-+30*
	6,8	С	0,06	2,4	1,8	B45196-H6685-+30*
	15	D	0,06	5,3	0,8	B45196-H6156-+40*
	22	E	0,06	7,7	0,8	B45196-H6226-+50*
	332)	E	0,06	11,6	0,8	B45196-H6336-+50*
50	0,15	Α	0,04	0,5	22	B45196-H7154-+10*
(33)	0,22	Α	0,04	0,5	18	B45196-H7224-+10*
	0,47	В	0,04	0,5	9,0	B45196-H7474-+20*
	1,5	С	0,06	0,8	4,4	B45196-H7155-+30*
	6,8	E	0,06	3,4	0,8	B45196-H7685-+50*
	102)	E	0,06	5,0	0,8	B45196-H7106-+50*

²⁾ Upon request





For characteristic curves see page 37/38 ff

V _R up to 85°C (up to 125°C)	C _R	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned teminals
4	3,3	Α	0,045	0,5	5,9	B45196-P335-+10*
(2,5)	4,7	Α	0,045	0,5	4,6	B45196-P475-+10*
	6,8	Α	0,045	0,5	3,9	B45196-P685-+10*
	10	В	0,045	0,5	2,7	B45196-P106-+20*
	15	В	0,045	0,6	2,6	B45196-P156-+20*
	22	С	0,045	0,9	1,7	B45196-P226-+30*
	33	С	0,045	1,3	1,5	B45196-P336-+30*
	47	С	0,045	1,9	1,1	B45196-P476-+30*
	68	D	0,045	2,7	0,8	B45196-P686-+40*
	100	D	0,06	4,0	0,6	B45196-P107-+40*
	150	D	0,06	6,0	0,6	B45196-P157-+40*
6,3	2,2	Α	0,045	0,5	6,5	B45196-P1225-+10*
(4)	3,3	Α	0,045	0,5	4,6	B45196-P1335-+10*
	4,7	Α	0,045	0,5	3,6	B45196-P1475-+10*
	6,8	В	0,045	0,5	2,7	B45196-P1685-+20*
	10	В	0,045	0,6	2,1	B45196-P1106-+20*
	15	С	0,045	1,0	1,7	B45196-P1156-+30*
	22	С	0,045	1,4	1,3	B45196-P1226-+30*
	33	С	0,045	2,1	1,1	B45196-P1336-+30*
	47	С	0,045	3,0	0,8	B45196-P1476-+30*
	47	D	0,045	3,0	0,8	B45196-P1476-+40*
	68	D	0,045	4,3	0,6	B45196-P1686-+40*
	100	D	0,06	6,3	0,6	B45196-P1107-+40*

Types B 45 196-P can be operated at temperatures up to 150 °C. Details for this operating condition must be agreed upon between supplier and customer.

¹⁾ Replace 196 by 198 for gold-plated terminals

⁺ Insert code letter for required capacitance tolerance: M = \pm 20 %, K = \pm 10 % (J = \pm 5 % upon request) * Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm





V _R up to 85°C (up to 125°C)	C _R	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned teminals
10	1,5	Α	0,045	0,5	6,5	B45196-P2155-+10*
(6,3)	2,2	Α	0,045	0,5	4,6	B45196-P2225-+10*
	3,3	Α	0,045	0,5	3,6	B45196-P2335-+10*
	4,7	В	0,045	0,5	2,7	B45196-P2475-+20*
	6,8	В	0,045	0,7	2,1	B45196-P2685-+10*
	10	В	0,045	1,0	1,8	B45196-P2106-+20*
	10	С	0,045	1,0	1,7	B45196-P2106-+30*
	15	С	0,045	1,5	1,4	B45196-P2156-+30*
	22	С	0,045	2,2	1,1	B45196-P2226-+30*
	33	D	0,045	3,3	0,8	B45196-P2336-+40*
	47	D	0,045	4,7	0,6	B45196-P2476-+40*
	68	D	0,045	6,8	0,6	B45196-P2686-+40*
	100	D	0,06	10	0,6	B45196-P2107-+40*
16	1,0	Α	0,030	0,5	6,5	B45196-P3105-+10*
(10)	1,5	Α	0,045	0,5	5,2	B45196-P3155-+10*
	2,2	Α	0,045	0,5	4,3	B45196-P3225-+10*
	3,3	В	0,045	0,6	3,0	B45196-P3335-+20*
	4,7	В	0,045	0,8	2,1	B45196-P3475-+20*
	6,8	С	0,045	1,1	1,7	B45196-P3685-+30*
	10	С	0,045	1,6	1,4	B45196-P3106-+30*
	15	С	0,045	2,4	1,1	B45196-P3156-+30*
	22	С	0,045	3,6	1,0	B45196-P3226-+30*
	22	D	0,045	3,6	0,8	B45196-P3226-+40*
	33	D	0,045	5,3	0,7	B45196-P3336-+40*
	47	D	0,045	7,5	0,6	B45196-P3476-+40*

Types B 45 196-P can be operated at temperatures up to 150 °C. Details for this operating condition must be agreed upon between supplier and customer.

¹⁾ Replace 196 by 198 for gold-plated terminals

⁺ Insert code letter for required capacitance tolerance: $M = \pm 20 \%$, $K = \pm 10 \%$ ($J = \pm 5 \%$ upon request)

^{*} Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm





V _R up to 85°C (up to 125°C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned teminals
20	0,68	Α	0,030	0,5	7,8	B45196-P4684-+10*
(13)	1,0	Α	0,030	0,5	5,9	B45196-P4105-+10*
	1,5	Α	0,045	0,5	5,2	B45196-P4155-+10*
	2,2	В	0,045	0,5	3,6	B45196-P4225-+20*
	3,3	В	0,045	0,7	2,7	B45196-P4335-+20*
	4,7	С	0,045	1,0	1,7	B45196-P4475-+30*
	6,8	С	0,045	1,4	1,3	B45196-P4685-+30*
	10	С	0,045	2,0	1,1	B45196-P4106-+30*
	15	D	0,045	3,0	0,9	B45196-P4156-+40*
	22	D	0,045	4,4	0,7	B45196-P4226-+40*
	33	D	0,045	6,6	0,6	B45196-P4336-+40*
25	0,47	Α	0,030	0,5	8,5	B45196-P5474-+10*
(16)	0,68	Α	0,030	0,5	6,5	B45196-P5684-+10*
	1,0	Α	0,030	0,5	5,2	B45196-P5105-+10*
	1,5	В	0,045	0,5	4,2	B45196-P5155-+20*
	2,2	В	0,045	0,6	3,0	B45196-P5225-+20*
	3,3	С	0,045	0,9	2,0	B45196-P5335-+30*
	4,7	С	0,045	1,2	1,6	B45196-P5475-+30*
	6,8	С	0,045	1,7	1,4	B45196-P5685-+30*
	10	С	0,045	2,5	1,1	B45196-P5106-+30*
	10	D	0,045	2,5	0,9	B45196-P5106-+40*
	15	D	0,045	3,8	0,7	B45196-P5156-+40*
	22	D	0,045	5,5	0,6	B45196-P5226-+40*

Types B 45 196-P can be operated at temperatures up to 150 °C. Details for this operating condition must be agreed upon between supplier and customer.

¹⁾ Replace 196 by 198 for gold-plated terminals

⁺ Insert code letter for required capacitance tolerance: M = \pm 20 %, K = \pm 10 % (J = \pm 5 % upon request) * Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm





$\overline{V_{\rm R}}$ up to 85 °C (up to 125 °C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	Z _{max} (20°C, 100 kHz)	Ordering code 1)
Vdc	μF			μΑ	Ω	Tinned teminals
35	0,10	Α	0,030	0,5	28	B45196-P6104-+10*
(23)	0,15	Α	0,030	0,5	23	B45196-P6154-+10*
	0,22	Α	0,030	0,5	15	B45196-P6224-+10*
	0,33	Α	0,030	0,5	11	B45196-P6334-+10*
	0,47	В	0,030	0,5	8,0	B45196-P6474-+20*
	0,68	В	0,030	0,5	5,5	B45196-P6684-+20*
	1,0	В	0,030	0,5	4,4	B45196-P6105-+20*
	1,5	С	0,045	0,6	3,3	B45196-P6155-+30*
	2,2	С	0,045	0,8	2,2	B45196-P6225-+30*
	3,3	С	0,045	1,2	1,7	B45196-P6335-+30*
	4,7	D	0,045	1,7	1,0	B45196-P6475-+40*
	6,8	D	0,045	2,4	0,9	B45196-P6685-+40*
	10	D	0,045	3,5	0,7	B45196-P6106-+40*
50	0,10	Α	0,030	0,5	27	B45196-P7104-+10*
(33)	0,15	В	0,030	0,5	22	B45196-P7154-+20*
	0,22	В	0,030	0,5	15	B45196-P7224-+20*
	0,33	В	0,030	0,5	11	B45196-P7334-+20*
	0,47	С	0,030	0,5	6,5	B45196-P7474-+30*
	0,68	С	0,030	0,5	5,5	B45196-P7684-+30*
	1,0	С	0,030	0,5	3,3	B45196-P7105-+30*
	1,5	D	0,045	0,8	2,8	B45196-P7155-+40*
	2,2	D	0,045	1,1	2,0	B45196-P7225-+40*
	3,3	D	0,045	1,7	1,1	B45196-P7335-+40*
	4,7	D	0,045	2,4	0,9	B45196-P7475-+40*

Types B 45 196-P can be operated at temperatures up to 150 °C. Details for this operating condition must be agreed upon between supplier and customer.



For characteristic curves see page 37/40 ff

V _R up to 85°C	C_{R}	Case size	tan δ _{max} (20°C,	I _{lk, max} (20°C, V _R ,	ESR _{max} (20°C,	/ac (20°C,	Ordering code ¹⁾
(up to 125°C)			120 Hz)	5 min)	100 kHz)	100 kHz)	
Vdc	μF			μΑ	mΩ	Α	Tinned terminals
6,3	22	С	0,06	1,4	375	0,54	B45197-A1226-+30*
(4)	33	С	0,06	2,1	350	0,56	B45197-A1336-+30*
	68	D	0,06	4,3	175	0,93	B45197-A1686-+40*
	100	D	0,08	6,3	125	1,10	B45197-A1107-+40*
	150	E	0,08	9,5	100	1,28	B45197-A1157-+50*
	220	E	0,08	13,9	100	1,28	B45197-A1227-+50*
	330	E	0,08	20,8	100	1,28	B45197-A1337-+50*
10	15	С	0,06	1,5	400	0,52	B45197-A2156-+30*
(6,3)	22	С	0,06	2,2	375	0,54	B45197-A2226-+30*
	47	D	0,06	4,7	200	0,87	B45197-A2476-+40*
	68	D	0,06	6,8	150	1,00	B45197-A2686-+40*
	100	D	0,08	10	100	1,22	B45197-A2107-+40*
	100	E	0,08	10	100	1,28	B45197-A2107-+50*
	150	E	0,08	15	100	1,28	B45197-A2157-+50*
	220	E	0,08	22	100	1,28	B45197-A2227-+50*
	330	E	0,08	33	100	1,28	B45197-A2337-+50*
16	10	С	0,06	1,6	450	0,49	B45197-A3106-+30*
(10)	15	С	0,06	2,4	400	0,52	B45197-A3156-+30*
	33	D	0,06	5,3	200	0,87	B45197-A3336-+40*
	47	D	0,06	7,5	175	0,93	B45197-A3476-+40*
	68	E	0,06	10,9	150	1,05	B45197-A3686-+50*
	100	E	0,08	16	100	1,28	B45197-A3107-+50*
20	6,8	С	0,06	1,4	475	0,48	B45197-A4685-+30*
(13)	10	С	0,06	2,0	450	0,49	B45197-A4106-+30*
	22	D	0,06	4,4	200	0,87	B45197-A4226-+40*
	33	D	0,06	6,6	200	0,87	B45197-A4336-+40*
	33	E	0,06	6,6	200	0,91	B45197-A4336-+50*
	47	E	0,06	9,4	150	1,05	B45197-A4476-+50*
	68	E	0,06	13,6	150	1,05	B45197-A4686-+50*

¹⁾ Replace 197-A by 198-R for gold-plated terminals + Insert code letter for required capacitance tolerance: M = ± 20 %, K = ± 10 % (J = ± 5 % upon request)

^{*} Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm



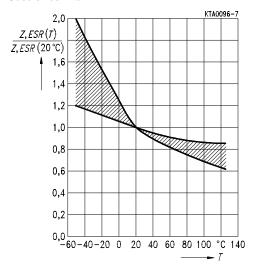
V _R up to 85°C (up to 125°C)	C_{R}	Case size	tan δ _{max} (20°C, 120 Hz)	I _{lk, max} (20°C, V _R , 5 min)	ESR _{max} (20°C, 100 kHz)	<i>l</i> ac (20°C, 100 kHz)	Ordering code ¹⁾
Vdc	μF			μΑ	mΩ	Α	Tinned terminals
25	4,7	С	0,06	1,2	525	0,46	B45197-A5475-+30*
(16)	15	D	0,06	3,8	230	0,81	B45197-A5156-+40*
	22	D	0,06	5,5	230	0,81	B45197-A5226-+40*
	22	E	0,06	5,5	230	0,85	B45197-A5226-+50*
	33	E	0,06	8,3	200	0,91	B45197-A5336-+50*
35	3,3	С	0,06	1,2	550	0,45	B45197-A6335-+30*
(23)	4,7	D	0,06	1,6	300	0,71	B45197-A6475-+40*
	6,8	D	0,06	2,4	300	0,71	B45197-A6685-+40*
	6,8	E	0,06	2,4	300	0,74	B45197-A6685-+50*
	10	D	0,06	3,5	260	0,76	B45197-A6106-+40*
	10	E	0,06	3,5	260	0,80	B45197-A6106-+50*
	15	D	0,06	5,3	260	0,76	B45197-A6156-+40*
	15	E	0,06	5,3	260	0,80	B45197-A6156-+50*
	22	E	0,06	7,7	260	0,80	B45197-A6226-+50*
50	4,7	D	0,06	2,4	300	0,71	B45197-A7475-+40*
(33)	6,8	E	0,06	3,4	300	0,74	B45197-A7685-+50*

Replace 197-A by 198-R for gold-plated terminals
 Hosert code letter for required capacitance tolerance: M = ± 20 %, K = ± 10 % (J = ± 5 % upon request)
 Insert code number for required reel diameter: 9 = 180 mm, 6 = 330 mm



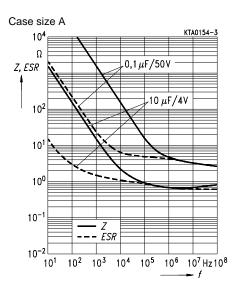
Impedance Z and equivalent series resistance ESR versus temperature T Typical behavior

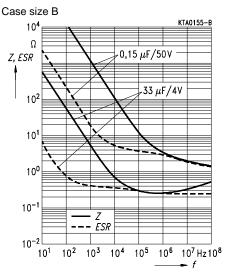
Case sizes A to E

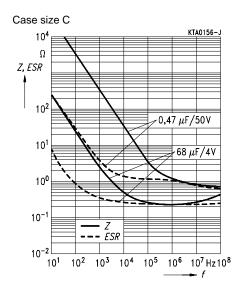


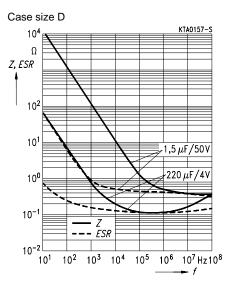


Impedance Z and equivalent series resistance ESR versus frequency f Typical behavior





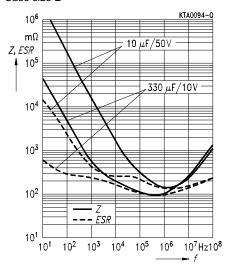






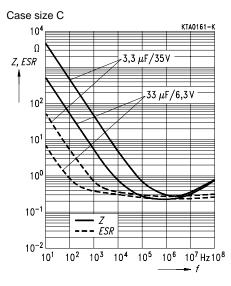
Impedance Z and equivalent series resistance ESR versus frequency f Typical behavior

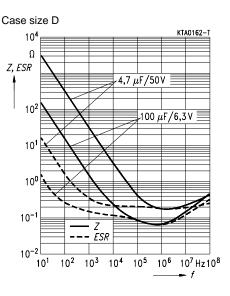
Case size E



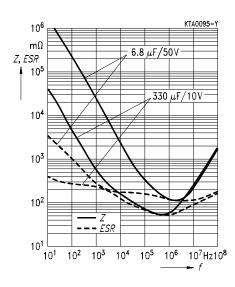


Impedance Z and equivalent series resistance ESR versus frequency f Typical behavior



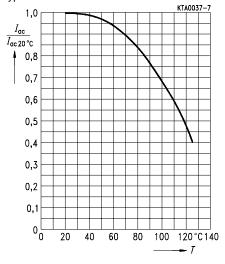


Case size E

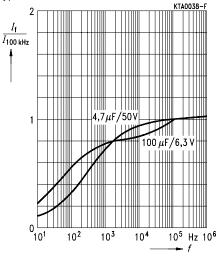




Permissible ripple current versus temperature *T* Typical behavior



Permissible ripple current versus frequency *f* Typical behavior





Siemens Matsushita Components

Ceramic chip capacitors from stock

Small in size, big in performance

Our selection of capacitors ranges from standard sizes down to a miniature highlight in 0402 style. Measuring only 1 x 0.5 x 0.5 mm, it's an ideal solution for applications where space is tight, like in handies and cardiac pacemakers. At the same time all our chips can boast excellent soldering characteristics, with special terminal variants for conductive adhesion. And we also thought about the right packing for automatic placement. You get all sizes down to 1206 in bulk case for example, plus voltage ratings from 16 to 500 V. By the way, our leaded models have CECC approval of course, in fact they were certified more than ten years ago.

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General Technical Information

1 Basic construction

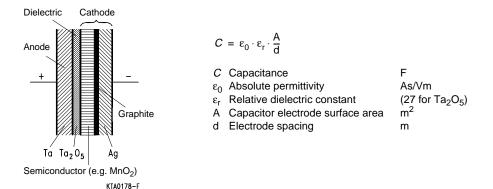


Fig. 1 Basic construction of a tantalum capacitor and calculation of capacitance

Anode	Body of sintered tantalum powder			
Dielectric	Tantalum oxide, generated electrochemically by oxidation on the anode			
Cathode	Semi-conducting metal oxide (manganese dioxide) deposited on the anodic oxide foil			
Contacts to the cathode	Graphite and conducting silver layer that is applied on the semi-conducting coating and soldered or glued to the case or the terminals			

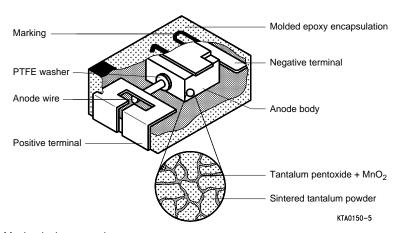


Fig. 2 Mechanical construction

2 Polarity

Tantalum electrolytic capacitors are polar capacitors. The dielectric layers of polar electrolytic capacitors are arranged so that the current is blocked only in one direction. It is important, therefore, to pay attention to the polarity marking (positive pole on anode, negative pole on cathode). Reverse polarity is permitted only up to the values indicated on page 47, otherwise the capacitor may be destroyed by the effects of the rapidly increasing current.

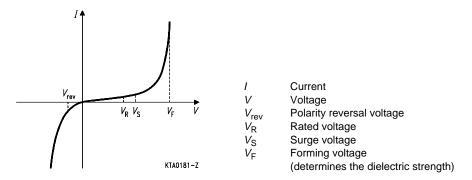


Fig. 3 Current/voltage curve of a tantalum electrolytic capacitor (qualitative depiction only)

3 Standards

The tantalum electrolytic capacitors described in this data book are all designed for enhanced service requirements. The mechanical and electrical characteristics, together with the corresponding tests and test procedures, are set down in the appropriate standards. With regard to the technical content, the general IEC, CECC and DIN standards are in line with each other.

General standards for tantalum electrolytic capacitors

Tantalum chip capacitors

IEC 384-1	(identical with DIN IEC 384, part 1, CECC30000 and DIN 45 910) Generic specification: Fixed capacitors for use in electronic equipment
IEC 384-3	(identical with DIN IEC 384, part 3, CECC30800 and DIN 45 910 part 15) Sectional specification: Tantalum chip capacitors
IEC 384 - 3-1	(identical with DIN IEC 384, part 3-1 and CECC30801) Blank detail specification:

Assignment of tantalum electrolytic capacitors produced by S + M Components to existing detail specifications

Detail specification	Series
CECC30801-801	B 45 196-E, B 45 196-P
IEC-QC300801/US0001	B 45 196-E, B 45 196-P
CECC30801-802	B 45 196-H
CECC30801-011	B 45 196-Q (special version)
CECC30801-805	B45197

4 Voltages

4.1 Rated voltage

The rated voltage V_R is the dc voltage indicated upon the capacitor. It determines the thickness of the dielectric.

4.2 Maximum continuous voltage

The maximum continuous voltage $V_{\rm cont}$ is the maximum permissible voltage at which the capacitor can be continuously operated. It is a direct current voltage, or the sum of the basic dc voltage plus the peak value of the superimposed ac voltage (cf. chapter 7, on page 52).

The maximum continuous voltage depends on the ambient temperature (cf. figure 4). Within the temperature range of -55 to +85 °C, the rated voltage is equal to the maximum continuous voltage for tantalum electrolytic capacitors.

In the temperature range between + 85 and + 125 °C, the maximum continuous voltage must be reduced linearily from the rated voltage to $^2/_3$ of the rated voltage. 85 °C at $^2/_3$ V_R and 125 °C at $^2/_3$ V_R constitute approximately the same load for the capacitor. Operation below the maximum continuous voltage has a positive effect on the capacitor's service life.

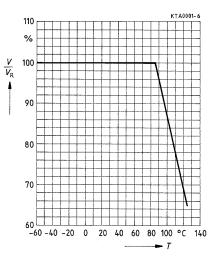


Fig. 4 Max. permissible continuous voltage (operating voltage) versus temperature

4.3 Operating voltage

The operating voltage $V_{\rm op}$ is the voltage applied to the capacitor during continuous operation. It is not allowed to exceed the max. continuous voltage.

All unfavorable operating conditions (e.g. possible line overvoltages, unfavorable tolerances of the transformation ratio of the line transformer in the equipment, repeated overvoltages upon switching equipment on, high ambient temperatures etc.) have to be taken into account when determining the operating voltage.

4.4 Surge voltage

The surge voltage $V_{\rm S}$ is the maximum voltage (peak value) which may be applied to the capacitor for short periods, at the most 5 times for a duration of up to 1 minute per hour. The surge voltage must not be applied for periodic charging and discharging in the course of normal operation.

The permissible surge voltage for all capacitors in this data book is

$$1,3 \cdot V_R$$

If voltage impulses (transient voltages) exceeding the surge voltage occur, this may lead to irreparable damage.

If applications of this kind are planned, please consult us first.

4.5 Polarity reversal voltage (incorrect polarity)

Any incorrect polarity resulting from the sum of the direct voltage and the alternating voltage components must be smaller than or equal to the permitted polarity reversal voltage (see table below). To avoid reducing the reliability, this voltage may only occur for a short time, at most five times for a duration of one minute per hour.

Permissible polarity reversal voltage for capacitors with a solid electrolyte:

```
at 20 °C: 0,15 \cdot V_R
at 55 °C: 0,10 \cdot V_R
at 85 °C: 0,05 \cdot V_R
at 125 °C: 0,03 \cdot V_R
```

4.6 Series back-to-back connection

For applications where higher polarity reversal voltages occur, two capacitors with identical rated voltage and identical rated capacitance can be connected back-to-back in series (e.g. cathode to cathode). In this way, blocking in each polarization direction is achieved. To avoid damage to the reversed polarity capacitors during charging, it is necessary to connect diodes in parallel to the capacitors with the center of the diodes and the capacitors connected together.

This non-polar or bi-polar version (which only has half the capacitance value as a result) can be operated at voltages of up to the rated direct voltage of any polarity or with double the superimposed alternating voltage of the value permitted for the individual capacitor.

The capacitors connected back-to-back in this way can also be operated at a pure ac voltage. The surface temperature of the capacitor is not allowed to increase by more than max. 10 $^{\circ}$ C, while the upper temperature limit should not be exceeded.

4.7 Inherent voltage

Occasionally, inherent voltages can occur in electrolytic capacitors (due to element formation between anode and cathode). Since these inherent voltages are relatively low (< 0,5 V), with accordingly high internal resistance (several $10^6 \Omega$), they are of no importance for most applications.

4.8 Recharging

In all conventional capacitors a recharging effect may occur. This effect causes a charged capacitor to generate a recharging voltage that is of the same polarity as the charging voltage after external bridges are removed from the capacitor's layers. The recharging voltage is more or less independent of the capacitance of the capacitors and the thickness of the dielectric and can be considered to be a characteristic property of the dielectric material.

The value of the recharging voltage depends on various factors (type, charging time, discharging time, time of measurement, ambient temperature) and can attain an order of magnitude of 10⁻² up to several tenths of the operating voltage. Of all electrolytic capacitors, capacitors with solid electrolytes have the lowest recharging effect.

5 Capacitance

5.1 Rated capacitance

The rated capacitance C_R is the capacitance value by which the capacitor is identified. The actual capacitance of a capacitor can deviate from the rated capacitance by as much as the full magnitude of the tolerance at delivery.

The capacitance of tantalum electrolytic capacitors is determined at a frequency of 120 Hz and a temperature of 20 $^{\circ}$ C as a series capacitance in an alternating current bridge circuit with measuring voltages < 0,5 $V_{\rm rms}$.

5.2 Capacitance tolerance

The capacitance tolerance (or tolerance at delivery) $\Delta C/C_R$ is the maximum permitted deviation of the actual capacitance value from the specific rated capacitance.

Where the capacitance tolerances are to be indicated on the components themselves, S + M Components uses code letters in accordance with IEC 68. This code letter also forms part of the ordering code.

5.3 Temperature dependence of the capacitance

The capacitance of a tantalum electrolytic capacitor varies with the temperature (positive temperature coefficient), cf. figure 5. The amount by which it varies depends on the voltage and capacitance value. Low voltages and high capacitance values cause greater variations than high voltages and low capacitance values.

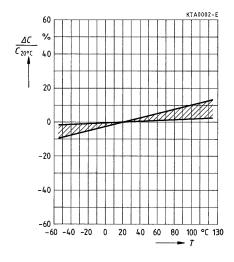


Fig. 5 Capacitance change versus temperature (typical values)

	– 55 °C	+ 85 °C	+ 125 °C
Maximum values for chip capacitors	– 10 %	+ 10 %	+ 12 %

5.4 Frequency dependence of the capacitance

The capacitance decreases with increasing frequency. A typical curve is shown in figure 6.

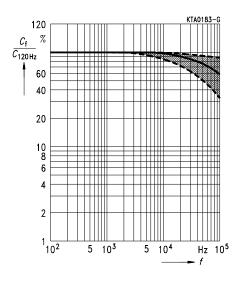


Fig. 6 Capacitance change versus frequency (typical behavior), reference temperature 20 °C

Typical values of the effective capacitance can be derived from the impedance curve, as long as the impedance is still in the range where the capacitive component dominates.

$$C = \frac{1}{2 \cdot \pi \cdot f \cdot Z}$$

C Capacitance F

f Frequency

Z Impedance Ω

5.5 Charge-discharge proof

Hz

Tantalum electrolytic capacitors produced by S + M Components are charge-discharge proof. This means that the capacitance reduction after 10⁸ charge-discharge cycles will be less than 3 %.

General Technical Information

6 Impedance / Equivalent series resistance (ESR)

The impedance can also be described as the absolute value of the ac resistance.

The impedance of tantalum electrolytic capacitors is represented in close approximation by a series circuit comprising the following individual resistance components:

- 1. Effective reactance $1/\omega C$ of the capacitance C
- Dielectric losses and the ohmic resistance of the electrolyte and/or the semiconductor layer (equivalent series resistance ESR)
- 3. Effective reactance ωL of the inductance of the electrodes and the terminals.

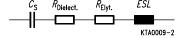


Fig. 7 Simplified equivalent circuit diagram of a tantalum electrolytic capacitor

The frequency and temperature relationship of these components determine the impedance characteristics.

ESR includes both $R_{\mathrm{Dielect}} + R_{\mathrm{Elyt}}$: R_{Dielect} represents the dielectric losses and decreases with $1/\omega$. R_{elyte} represents the series resistance of the electrolyte and does not vary with frequency. R_{Dielect} is negligible at frequencies above approximately 10 kHz.

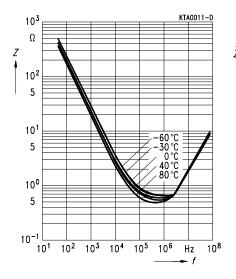
In the higher and lower frequency ranges, the frequency characteristic of the impedance is mainly caused by both the reactances. The temperature characteristic is mainly determined by the resistance of the electrolyte.

Because of the corrosion-resistance of tantalum, highly conductive electrolytes can be used for tantalum electrolytic capacitors, so that these capacitors have low series resistances. A particularly high conductivity is achieved by the solid semiconductor layer used instead of a liquid electrolyte. Hence capacitors with solid electrolytes have the lowest series resistance of all electrolytic capacitors.

The conductivity of the electrolyte varies only slightly, even at low temperatures. This means that the impedance of tantalum electrolyte capacitors displays favorable frequency and temperature characteristics.

The following figures show the typical behavior of the impedance in relationship to frequency and temperature.

The decrease in impedance at low frequencies down to a few kHz is determined by the capacitive reactance, whereas the following, almost horizontal course of the curve mainly shows the ohmic series resistance. Beyond the natural resonant frequency, the inductive reactance becomes increasingly predominant, so that the curves finally merge into straight lines.



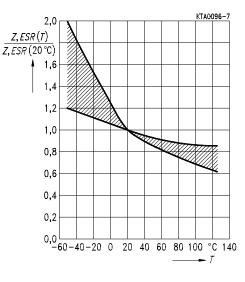


Fig. 8 Impedance Z versus frequency f
Capacitor 6,8 μF/35 Vdc

Fig. 9 Impedance Zversus temperature T

General Technical Information

7 AC power dissipation

7.1 Superimposed alternating voltage for capacitors with solid electrolyte

The superimposed alternating voltage is the r.m.s. alternating voltage that may be applied to a capacitor in addition to a direct voltage. The sum of the direct voltage and the peak value of the superimposed alternating voltage must not exceed the maximum continuous voltage. The superimposed alternating voltage must be limited in such a way that no unpermitted incorrect polarity occurs (permissible incorrect polarity, cf. chapter 4.5).

The alternating current flowing through the capacitor or the alternating voltage applied may not exceed a maximum value determined for the respective type and rated capacitance, since the capacitor might be damaged due to overheating or its service life may be reduced. The value of the permitted alternating voltage and/or the superimposed alternating current depends on the equivalent series resistance (*ESR*) and the permissible power dissipation. Here, the permitted inherent heating for the respective type is taken into consideration.

The basis for the calculations are as follows:

$$P = I^2 \cdot ESR$$
, with $I = \frac{V}{Z}$ we obtain

$$P = \frac{V^2 \cdot ESR}{Z^2}$$

Ρ	Power dissipation	W
1	Effective ripple current	Α
V	Effective alternating voltage	Vac
Z	Impedance	Ω
ESR	Equivalent series resistance	Ω

7.2 Maximum permissible ripple current and alternating voltage loads

Using P_{\max} from the following tables, the maximum permissible ripple current and alternating voltage loads can be calculated.

$$I_{\text{max}} = \sqrt{\frac{P_{\text{max}}}{ESR}}$$
 $V_{\text{max}} = Z\sqrt{\frac{P_{\text{max}}}{ESR}}$

7.2.1 Maximum permissible power dissipation with ripple current load

Case size	Z	Р	Α	В	С	D	E
P _{V max} in mW	28	55	75	85	110	150	165

Reduction of the calculated values versus the ambient temperature, cf. figure 10.

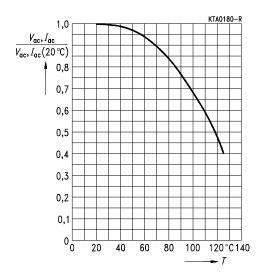


Fig. 10 Permissible ripple current $\it I$ ac and permissible alternating voltage $\it V$ ac versus temperature $\it T$

General Technical Information

8 Dissipation factor

The dissipation factor $\tan \delta$ increases with frequency and tends to very high values at near-resonance frequencies. The figures below show the typical frequency and temperature behavior of the dissipation factor.

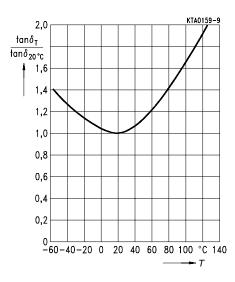


Fig. 11 Dissipation factor versus temperature at f = 120 Hz

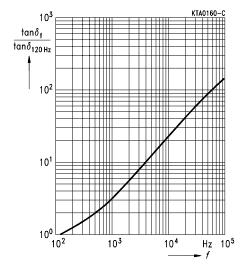


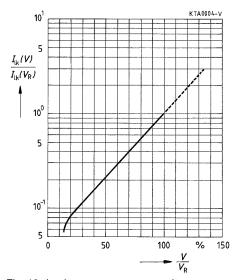
Fig. 12 Dissipation factor versus frequency at T = 20 °C

9 Leakage current

When a direct voltage is applied to electrolytic capacitors, a low, constant current will flow through any capacitor. This so-called leakage current I_{lk} is a function of the voltage as well as of the temperature. (Graphs are shown in chapter 9.1).

The value of the leakage current of an electrolytic capacitor is determined, above all, by the impurities (atoms of foreign substances that cannot be formed) in the carrier metal (anode). The use of high-purity tantalum powder results in a low fault density in the dielectric and thus in a low leakage current.

9.1 Temperature and voltage dependence of the leakage current



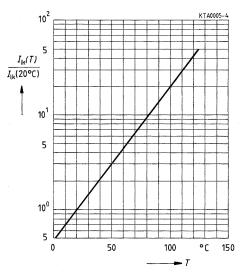


Fig. 13 Leakage current versus voltage

Fig. 14 Leakage current versus temperature

9.2 Time dependence of the leakage current

As can be seen from figure 15, the leakage current is high when the voltage is first applied (inrush current). This decreases rapidly, however, in the course of operation and finally achieves an almost constant "steady-state" value.

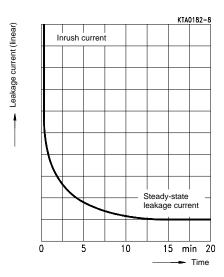


Fig. 15 Leakage current versus time for which a voltage is applied

9.3 Leakage current measurement

The leakage current is measured at 20 $^{\circ}$ C, after the rated voltage has been applied to the capacitors for five minutes. A stabilized power supply is required and a series resistor of 1000 Ω should be connected in order to limit the charging current.

Before the voltage is applied, the capacitors must be stabilized at the rated temperature for 30 minutes.

For tantalum capacitors with solid electrolyte, the following limit value at 20 °C is required by the applicable standards:

$$I_{\rm lk} \, \leq \, 0.01 \, \, \mu {\rm A} \cdot \left(\frac{C_{\rm R}}{\mu {\rm F}} \cdot \frac{V_{\rm R}}{V} \right) \! , \, {\rm minimum} \, \, 0.5 \, \, \mu {\rm A} \, . \label{eq:lk}$$

The following temperature factors apply

at 85 °C: 10

at 125 °C: 12,5

9.4 Leakage current behavior after storage without applied voltage

Tantalum and its oxide are extremely resistant to chemical influences and are only attacked by very aggressive chemicals. They possess a high resistance to commonly used electrolytes and there is no deterioration of the oxide layer.

Storage without an applied voltage at room temperature has no effect on the leakage current and only a slight effect at increased storage temperatures. This means that tantalum electrolytic capacitors can be stored for at least 10 years without requiring subsequent regeneration.

10 Resistance to climatic stress

Both for reasons of reliability and due to the fact that the electrical parameters vary with temperature, limits must be set for the climatic conditions to which tantalum capacitors are subjected. The most important climatic factors are the permissible minimum and maximum temperatures and humidity conditions. The values for these three factors are coded as IEC climatic categories (cf. chapter 10.5). The IEC category applicable to each type is given in the corresponding data sheet.

10.1 Temperature range

The temperature range of a capacitor is the range between the lower and upper category temperatures within which the capacitor may be operated in accordance with its climatic category.

The temperature range for tantalum electrolytic capacitors lies between – 55 and + 125 °C.

Within the -55 to +85 °C range, the maximum continuous voltage $V_{\rm cont}$ may be equal to the rated voltage $V_{\rm R}$, provided that no other limiting conditions are specified. From 85 °C upwards, voltage reductions should be made (cf. chapter 4.2).

10.2 Minimum permissible operating temperature T_{min} (Lower category temperature)

The lower category temperature results from the capacitance decrease permitted for each individual capacitor type or from the increase in impedance due to the reduced conductivity of the electrolyte or the semiconductor layer. Temperatures down to the lower category temperature do not affect the service life.

10.3 Maximum permissible operating temperature T_{max} (Upper category temperature)

The upper category temperature is the maximum permissible ambient temperature at which a capacitor may be continuously operated at the stated permissible electrical load. If this limit is exceeded, the capacitor may fail prematurely. It is possible to exceed the upper category temperature for short periods. However, since the permissible period depends on the electrical load, it is essential to consult S + M Components before implementing such applications.

10.4 Damp heat conditions

The permissible damp heat conditions for tantalum electrolytic capacitors are specified by the climatic categories in accordance with IEC 68-1 and are proved by tests in accordance with IEC 68-2-3.

General Technical Information

10.5 IEC climatic category

The permissible climatic stress on a capacitor is given by the respective IEC climatic category. According to IEC 68-1, the climatic category comprises 3 groups of numbers, separated by slashes.

Example: 55/125/56

1st group: Lower category temperature (temperature limit) denoting the test temperature for test A (cold) in accordance with IEC 68-2-1.

2nd group: Upper category temperature (temperature limit) denoting the test temperature for test B (dry heat) in accordance with IEC 68-2-2.

3rd group: Number of days, the duration of test Ca (damp heat, steady state) at a relative humidity of 93 +2/-3 % and an ambient temperature of 40 °C, in accordance with IEC 68-2-3.

10.6 Storage and transportation temperatures

Tantalum capacitors with solid electrolyte may be stored at temperatures down to $-80\,^{\circ}\text{C}$.

The upper storage temperature may not exceed the rated temperature range.

11 Notes on mounting

For soldering tests, refer to the chapters "Measuring and Test Conditions" and "Soldering Conditions". These chapters also include layout recommendations and soldering temperature profiles.

11.1 Cleaning agents

The cleaning agents normally used nowadays for cleaning printed circuit boards after components have been soldered in can also be used, without restrictions, for tantalum electrolytic capacitors. Four-chamber ultrasonic cleaning processes with short individual stages and adequate subsequent drying provide good protection against damage.

12 Standard barcode label

The standard product package label provides barcode information as well as the usual text information. This provides advantages in the internal goods flow, but above all, it allows fast and accurate identity monitoring by the customer.

Due to our systematically constructed, unique marking on the packages, each component can be traced back to a certain production lot. This, in turn allows monitoring of the entire production procedure right back to the purchasing of raw materials.

The information includes the type, ordering code, quantity, date of manufacture, storage number, lot number and, where applicable, customer number. The barcode used is code 39 (medium density).

Example:



13 Packing

When packing our products, naturally we pay attention to the needs of the environment. This means that:

- only environmentally compatible materials are used for packing, and
- the amount of packing is kept to an absolute minimum.

In observing these rules, we are also complying to German packaging legislation.

In order to further comply to the aims of this legislation concerning the reduction of commercial waste, we have implemented the following measures:

- Standardized "Euro" pallets are used.
- Goods are secured on pallets using straps and edge protectors made of environmentally compatible plastics (PE or PP). No stretch or shrink-wrap foils are used.
- Shipping cartons (transport packaging) qualify for and carry the RESY logo.
- Separating layers between pallets and cartons are of a single material type, preferably paper or cardboard.
- Styrofoam (expanded polystyrene foam) chips are used as filler and padding materials. These can be re-used. They are expanded to a foam without using CFCs and halogens.
- The shipping cartons are sealed with paper adhesive tape in order to ensure that only a single, uniform material needs to be disposed of.
- We are prepared, in principle, to take back the packing material (especially product-specific plastic packages). However, we ask our customers to send cardboard cartons, corrugated cardboard, paper etc. to recycling or disposal companies in order to avoid unnecessary transportation of empty packing materials.

General Technical Information

14 End of use and disposal

All tantalum electrolytic capacitors produced by S + M Components are free of substances listed in German chemicals and CFC halogen prohibitive regulations. Nor do they contain any chemical substances of groups I through VIII of the Montreal clean air agreement or mentioned in EC regulation 3093/94.

Tantalum electrolytic capacitors are not categorized as waste materials requiring special supervision in recycling and waste disposal regulations. Consequently they may be left in electronic equipment and on circuit boards without any declaration or restriction and collected by an authorized disposal and recycling agency for electronic waste.

Customers outside Germany are requested to observe the disposal regulations which apply in their respective country.

15 Structure of the ordering code (part number)

All technical products produced by our company are identified by a part number (which is identical to the ordering code). This number is a unique identifier for any respective specific component that can be supplied by us. The customer can speed up and facilitate processing of his order by quoting the part number. All components are supplied in accordance with the part numbers ordered.

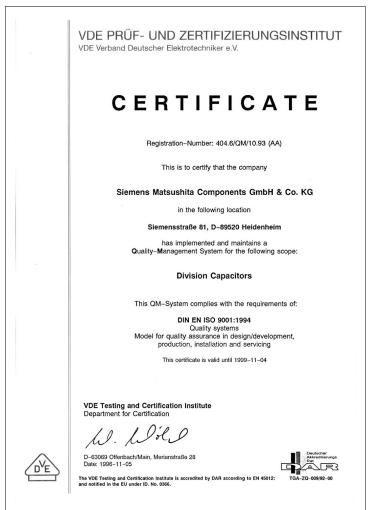
The structure of the ordering code is explained in the data sheet section (pages 12 and 18).

1 General

The high demands made of us by the world market for product and service quality make a comprehensive, thorough and up-to-date quality management system indispensable.

The QM system introduced in Capacitors Division was certified to EN ISO 9001 in June 1992. Numerous customer audits and awards are evidence of its efficiency and effectiveness.

The QM system has been further developed and refined in line with the requirements of standards (EN ISO 9000 ff, CECC) and EFQM criteria. The next objective is certification to QS 9000 and VDA 6.1.





1.1 Total quality management and zero defect concept

The strategic aim of Total Quality Management (TQM) is to satisfy the demands made by customers on products or services in terms of function, quality, punctuality and price/performance.

Based on the principle "quality from the very start", all instances and persons at S+M Components are involved in implementing this aim. Systematic planning, careful selection of suppliers and sure mastery of design and manufacturing processes are the major guarantees of a constantly high quality standard.

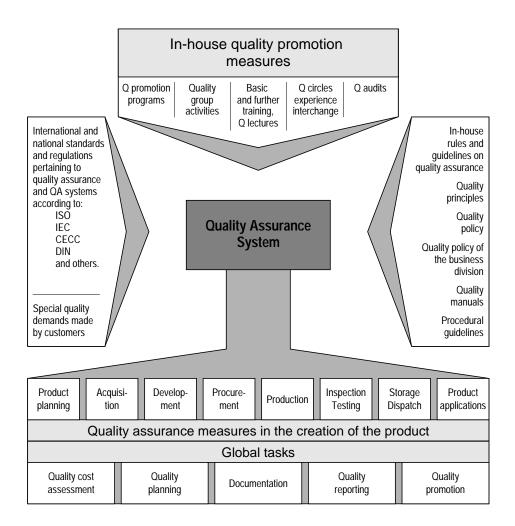
Internal quality promotion measures, such as training, quality groups, quality assurance circles and Q audits strengthen the feeling of responsibility in all employees, helping them to realize the significance of defects and thus avoid them.

Modern quality tools such as FMEA, SPC and Zero-Defect Programs with CEDAC¹⁾ diagrams supplement and support measures for quality assurance and enhancement.

Failure Mode and Effects Analyses Statistical Process Control Cause and Effect Diagram with Addition of Cards

¹⁾ FMEA SPC CEDAC

1.2 Quality assurance system



Quality Assurance

2 Quality assurance procedure

The quality department examines capacitors and releases them for production according to the following criteria:

- compliance with type specifications
- process capability of equipment
- measuring and test technique.

The entire production process – from procurement of parts and materials, through the fabrication process to final inspection – is accompanied by quality assurance measures. The flow chart (cf. 2.5) shows the quality inspections stipulated for each individual step.

2.1 Material procurement

The high quality of parts and materials required in the manufacture of high-grade products is achieved through close co-operation with suppliers. Focal aspects of these quality assurance measures are the choice and qualification of suppliers, harmonization of specifications, incoming-goods inspection, quality assessment and problem management.

2.2 Product quality assurance

All essential manufacturing processes are subjected to permanent monitoring. Critical parameters, in particular, are subjected to statistical process control (SPC).

So-called "QC gates" are planned into the manufacturing process, i.e. there is an inspection for release at the end of the corresponding step. The continuous monitoring and evaluation of the test results are used to assess procedures and to determine how well the processes are mastered.

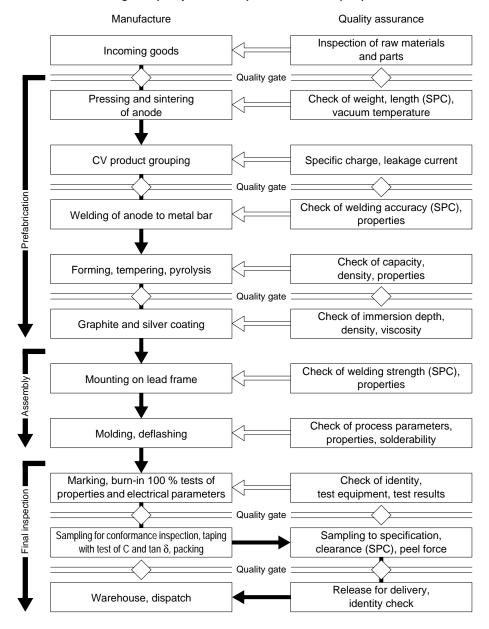
2.3 Final inspection

The capacitors are subjected to a specification-based final inspection. The parameters capacitance tolerance, dissipation factor / equivalent series resistance, impedance, leakage current and properties (i.e. mechanical finish) are checked.

2.4 Product monitoring

Our quality assurance department periodically carries out tests on random samples taken from current production lots to check climatic resistance, operational reliability, solderability and resistance to soldering heat in accordance with DIN, CECC and IEC specifications.

2.5 Manufacturing and quality assurance procedures for chip capacitors



Quality Assurance

3 Delivery quality

The term "delivery quality" is used to indicate conformance with the mutually agreed specifications at the time of delivery.

This conformance is monitored and guaranteed by quality assurance through constant sampling tests. Their accumulated results produce the AOQ (average outgoing quality) figures.

3.1 Random sampling

The AQL (AQL = acceptable quality level) figures given in section 3.3 are based on random sample inspection specification ISO 2859-1 single sampling plan for normal inspection, inspection level II. The contents of this standard correspond to MIL STD105 D and IEC 410.

The sampling instructions of this standard are such that a delivered lot will be accepted with a probability of ≥ 90 % if the percentage of non-conformancies does not exceed the stated AQL figure.

As a rule, the percentage of non-conformancies in deliveries from S+M Components is significantly below the AQL figure. The acceptance figure we apply to inoperatives, i.e. unusable components is c=0.

3.2 Classification of inoperatives / non-conformancies

A non-conformancy exists if a component characteristic fails to meet the data sheet specifications or an agreed delivery specification. Inoperatives are totally unusable components.

Inoperatives:

- short circuit or open circuit
- breakage of terminals or encapsulation
- wrong or missing marking
- wrong marking of terminals
- mixing with other components
- alternating orientation in one tape

Non-conformancies:

- non-conformancies in electrical characteristics (electrical characteristics outside of specified limits)
- non-conformancies in mechanical properties
 (e.g. wrong dimensions, damaged case, illegible marking, bent terminals).

3.3 AQL figures

The following AQL figures apply to the non-conformancies listed above:

inoperatives (electrical and mechanical)
 sum of electrical non-conformancies
 sum of mechanical non-conformancies
 0,25
 sum of mechanical non-conformancies

3.4 Incoming goods inspection

We recommend the use of a random sampling plan according to ISO 2859-1 (the contents correspond to MIL STD 105 D and IEC 410) for incoming goods inspection.

The test methods to be used are laid down in the relevant standards. Deviations must be agreed by the customer and the supplier. In case of complaints refer to section 7.

Single sampling plan for normal inspection – inspection level II Excerpt from ISO 2859–1:

Sampling plan	AQL	AQL	AQL	AQL
N = Lot size	0,065	0,10	0,15	0,25
2 50	N-0	N-0	N-0	N-0
51 90	N-0	N-0	N or 80-0	50-0
91 150	N-0	N or 125-0	80-0	50-0
151 280	N or 200-0	125-0	80-0	50-0
281 500	200-0	125-0	80-0	50-0
501 1 200	200-0	125-0	80-0	50-0
1 201 3 200	200-0	125-0	80-0	200-1
3 201 10 000	200-0	125-0	315-1	200-1
10 001 35 000	200-0	500-1	315-1	315-2

Columns 2 to 5:

Left-hand figure = sample size

Right-hand figure = acceptable inoperatives/non-conformancies

Classification of

inoperatives/non-conformancies: cf. paragraph 3.2

Constant improvement of our performance is a primary objective, especially optimization of product quality in close cooperation between producer and user. For this purpose we offer our customers the possibility of quality assurance agreeements.

4 Service life

The service life is defined as the time that passes before a given failure percentage is attained for the respective component. The failure percentage is the ratio of the number of failures to the total number of inspected capacitors of the respective type. The service life depends on the defect criteria applied and on the operating conditions, i.e. on the electrical and thermal stress to which the capacitor is subjected.

The service life values stated in this data book have been established by carrying out endurance tests and accelerated tests (e.g. increased temperature). They refer to an ambient temperature of 40 $^{\circ}$ C, rated voltage and a circuit resistance of \geq 3 Ω /V.

The service life increases:

- with decreasing ambient temperatures,
- with decreasing superimposed ac voltage,
- with decreasing operating voltage/rated voltage ratios
- with increasing circuit resistance (cf. paragraphs 5.3, 5.4)
- with decreasing operating temperature (cf. paragraph 5.3 and figure 2)

Quality Assurance

4.1 Failure criteria

Inoperatives: short-circuit or open circuit

Failure due to variation, i.e. unsatisfactory electrical characteristics:

```
- I > 5 \cdot I_{lk} + 5 \mu A
```

Z > 3 times the initial limit value

- $\tan \delta$ > 1,5 times the initial limit value

 $\begin{array}{lll} - \ \Delta C & \text{for } V \leq 16 \ \text{V:} + 10 \ \dots - 20 \ \% \\ - & \text{for } V > 16 \ \text{V:} + 10 \ \dots - 10 \ \% \end{array} \right\} \text{ beyond (initial) tolerances}$

5 Reliability

Data on long-term reliability under severe or moderate operating conditions are gained from endurance tests which are carried out continuously. The data are based on the failures registered for capacitors under a defined load, and long-term reliability of the individual types tested is based on a confidence level of 60%. Our reliability data result from very large numbers of component operating hours.

5.1 Failure rate (long-term failure rate)

The failure rate is defined as the failure percentage divided by a specified operating period. The failure rate is expressed in fit (failures in 10⁹ component hours) or as percentage of failures in 1000 hours.

1 fit =
$$1 \cdot 10^{-9}$$
/h (**fit** = **f**ailure **i**n **t**ime)

Example of a failure rate λ_{test} determined by a useful life test:

1) Number of components tested N = 8000

2) Operating hours $t_b = 25\,000\,\mathrm{h}$ 3) Number of failures n = 2

 $\lambda_{\text{test}} = \frac{n}{N} \cdot \frac{1}{t_h} = \frac{2}{8000} \cdot \frac{1}{25000h} = 10 \text{ fit} = 0,001 \%/1000 h.$

Failure rate specifications must include failure criteria, operating conditions and ambient conditions.

Usually the failure rate of components, when plotted against time, shows a characteristic curve with the following two periods:

I : early failure period, II: service period

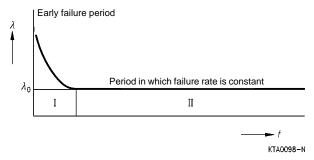


Fig. 1 Failure rate periods

Due to the 100 % burn-in tests, the early failure period (phase I) will coincide with the manufacturing process. Because of this, the failure rate relates to the service period (phase II). In phase II, an almost constant failure rate λ_0 can be expected, with a slight tendency to decrease with time.

5.2 Failure rate values

The failure rate (specified on page 19) refers to the following conditions.

Electrical load:

Operation at rated voltage

Circuit resistance $\geq 3 \Omega/V$

Climatic conditions:

Ambient temperature 40 °C, climatic class 3K3 in accordance with IEC 721,

non-corrosive atmosphere

Mechanical stress:

Class 3M3 in accordance with IEC 721

Service period:

Phase II as shown in figure 1.

Quality Assurance

These reference conditions do not always correspond to actual application conditions. For real applications, the failure rate must therefore be calculated as follows:

$$\lambda \, = \, \lambda_{\text{ref}} \cdot \pi_{\text{V}} \cdot \pi_{\text{T}} \cdot \pi_{\text{Rs}}$$

 λ_{ref} Failure rate under reference conditions

 π_V Factor for voltage dependence

 π_T Factor for temperature dependence

 π_{Rs} Factor for dependence on circuit resistance

5.3 Failure rate conversion factors

The failure percentage and failure rate are affected by the ambient temperature, the $V_{\rm op}/V_{\rm R}$ ratio, and, for capacitors with solid electrolyte, by the circuit resistance. These values increase with increasing ambient temperatures, and they are decreased by lowering the $V_{\rm op}/V_{\rm R}$ ratio and increasing the circuit resistance.

Conversion factors for taking into account the effects of ambient temperature and operating voltage on the failure rate within the service life can be deduced from the table below or from the graph in figure 2 (guideline values).

$V_{\rm op}/V_{\rm R}$	0,2	0,4	0,5	0,6	0,8	1
$\overline{\pi_{V}}$	3,5 · 10 ⁻⁴	2,4 · 10 ⁻³	6,5 · 10 ⁻³	1,7 · 10 ⁻²	0,13	1
	•					
T/°C	20	40	60	85	105	125
π_{T}	0,5	1	2,2	10	49	250
	•	•	•		•	•
Circuit res	sistance	Ω /V	≥ 3	1	0,3	≤ 0,1
π_{Rs}	$C_{R} \cdot V_{R}/\muC$	≤ 330	1	2	3,5	5
		> 330	1	2,8	6,1	12

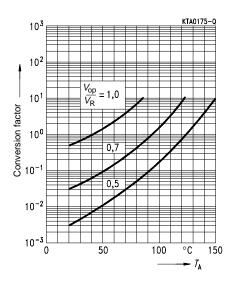


Fig. 2 Conversion factors for the failure rate

5.4 Effect of the circuit resistance (series resistance) on the failure rate of tantalum electrolytic capacitors with solid electrolytes

The employment of a certain series resistance is not a necessary condition for problem-free use of tantalum capacitors with solid electrolyte. However, it is possible to influence the service life by the series resistance value.

The failure rates specified in this book are based on circuits containing a series resistance (circuit resistance). In addition to the temperature and applied voltage, the series resistance too has an effect on the failure rate. The circuit resistance is defined as the total resistance as seen from the capacitor in the direction of the voltage source. It is the sum of the internal resistance of the voltage source, the wiring resistance and any additional resistors connected in series.

The circuit resistance is only of importance when there are localized dielectric breakdowns in the capacitor due to overloads. In such cases the circuit resistance limits the current and permits a self-healing of the capacitor. Tantalum capacitors with solid electrolyte have the ability to regenerate and heal internal defects.

The self-healing effect can only be successful if the breakdown occurs in a small area and if the energy dissipation in that area is limited while regeneration is taking place. If these conditions are not given, localized overheating may occur, leading to an expansion of the defect area and thus rendering regeneration impossible. While this self-healing process is taking place, the circuit resistance keeps the energy supply to a tolerable level, since the highly conductive solid electrolyte can take over this function only to a certain extent.

Thus the circuit resistance has a decisive effect on the self-healing process and, as a result, on the failure rate. The respective CECC standards contain corresponding information and data.

Quality Assurance

A current limit of approximately 300 mA (for limiting the energy dissipation) has been found to be suitable for enabling an effective self-healing process. This corresponds to a circuit resistance of 3 Ω /V. If the circuit resistance is lower, thus impairing the conditions for self-healing in the case of localized dielectric breakdown, then the failure rate increases, as expressed by the larger factors. In extreme cases, i.e. where the resistance approaches zero (\leq 0,1 Ω /V), the failure rate may increase to factor of approximately ten, depending on the case size.

5.5 Example of how to calculate the failure rate

Given:	ambient temperature	T_{A}	= 60 °C
	operating voltage	$V_{\rm op}$	= 25 Vdc
	circuit resistance	R_{S}^{sp}	\leq 0,1 Ω /V
capacitor us	ed (e. g. B 45 196-E):	C_{R}	= 1 μF
		V_{R}	= 50 Vdc
		failure i	ate = < 3 fit under refere

failure rate = \leq 3 fit under reference conditions.

For
$$\frac{V_{op}}{V_{R}}$$
 = 0,5 and T_{A} = 60 °C, a conversion factor of approximately 0,015 is deduced from

figure 2 on page 71. The same value is obtained from the table on page 70 by multiplying $\pi_V \cdot \pi_T$.

For a circuit resistance of \leq 0,1 Ω /V and $C_R \cdot V_R/\mu$ C \leq 330 the table gives a conversion factor of 5. Calculated failure rate: $\lambda = 3 \cdot 10^{-9}$ failures/h \cdot 0,015 \cdot 5 = 0,23 \cdot 10⁻⁹ failures/h = 0,23 fit.

5.6 Failure rate for B 45 194

As already explained, the failure rate varies with the operating conditions (ambient temperature, applied voltage, circuit resistance, application circuits etc.). Select the capacitors to obtain an adequate safety margin by fully examining the operating conditions.

The failure rate of B 45194 capacitors is given as percentage in 1000 hours and refers to rated voltage applied at 85 °C. Check the design objective with the following formula for the failure rate:

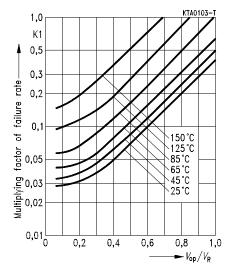
$$\lambda_{op} = \lambda_{85oC} \cdot K_t \cdot K_{sr}$$

 λ_{op} Predicted failure rate at operating conditions

 λ_{850C} Failure rate level at rated voltage and 85 °C: 1%/1000 h

 $K_{\rm t}$ Multiplying factor of failure rate as a function of maximum temperature and $\frac{V_{\rm op}}{V_{\rm R}}$, see figure 3

 $K_{\rm sr}$ Multiplying factor of failure rate as a function of cicuit resistance (Ω /V), see figure 4



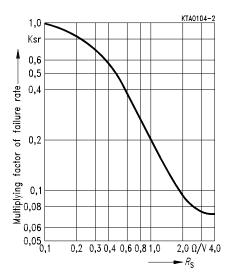


Fig. 3 Multiplying factor of failure rate versus temperature and voltage

Fig. 4 Multiplying factor of failure rate versus circuit resistance

Always consider safety when designing equipment and circuits. Plan for worst case failure modes such as short circuits and open circuits which could occur during use.

- Provide protection circuits and protection devices to allow safe failure modes.
- Design redundant and secondary circuits where possible to assure continued operation in case of main circuit failure.

6 Supplementary information

The specification of quality data – which always refer to a fairly large number of components – does not constitute a guarantee of characteristics or properties in the legal sense. However, agreement on these specifications does not mean that the customer may not claim for replacement of individual defective capacitors within the terms of delivery. S + M Components cannot, however, assume any further liability beyond the replacement of defective components. This applies in particular to any further consequences of component failure.

Furthermore, it must be taken into consideration that the figures stated for service life and failure rate refer to the average production status and are therefore to be understood as mean values (statistical expectations) for a large number of delivery lots of identical capacitors. These figures are based on application experience and on data obtained from preceding tests under normal conditions, or – for purposes of accelerated aging – more severe conditions.

Quality Assurance

7 Handling of claims and complaints

A main aim of our quality assurance system is to prevent any faults occurring. The following details will help us to respond quickly to any complaints which you may need to make:

- a) Non-conformancies (inoperatives) in incoming goods
- Description of non-conformancy
- Test method / circuit
- Sample size
- Number of non-conforming units found
- Proof unit
- Packing slip
- b) Non-conformancies (inoperatives) in production or operation
- Description of non-conformancy
- When and how was the non-conformancy detected
- Operating conditions
- Length of operation before non-conformancy occured
- Details as under a) if possible and applicable.

If transport damage has occurred, please describe it in detail and, if possible, mark it so that it can be distinguished from any other damage that may occur when the articles are returned. The original packing should also be examined and damage discovered should be described. To avoid further damage, please use the original packing, wherever possible, to return the articles being claimed for.

Measuring and Test Conditions

1 Test conditions selected from IEC 60-384-1

Endurance	IΔC/Cl	≤ 10 % of initial value
2000 h at +85 °C or	tan δ	≤ limit value
2000 h at +125 °C at reduced voltage	I _{lk20°C}	(in part 1,5 · limit value) ≤ 1,25 · initial limit value (in part 2 · limit value)
	Of 25 tested capa exceed the specific	citors, only one, at the most, may ed values.
Storage at high temperature	I∆C/Cl	≤ 10 % of initial value
without voltage applied	tan δ	≤ 1,5 · limit value
5000 h at +85 °C	I _{lk20°C}	≤ limit value
Damp heat, steady state*) in accordance with IEC 68-2-3	Severity 4:	40 (±2) °C; 93 (+2/-3) % relative humidity;
*) Increased test severity for chip capacitors	Duration:	56 days
B 45 196-P, see below	I∆C/CI	≤ 5 % of initial value
	tan δ	≤ 1,2 · limit value
	I _{lk20°C}	≤ initial limit value
Vibration	Frequency range:	10 2000 Hz
Test Fc in accordance with IEC 68-2-6	Amplitude:	1,5 mm (max. 196 m/s ² i.e. 20 <i>g</i>)
	Test duration: 6 h	,
Shock	Peak load :	981 m/s ² i.e. 100 g
Test Ea in accordance with IEC 68-2-27		

2 Tests with more stringent conditions than specified by CECC and IECQ for B 45 196-P

Damp heat, steady state	85 (+2) °C, 8 1000 h, at ra	5 90 % relative humidity, ited voltage
Parameter changes:	$I\Delta C/CI$ tan δ $I_{lk20^{\circ}C}$	≤ 10 % of initial value≤ 2 · initial limit value≤ 10 · initial limit value
Rapid change of temperature	100 cycles, - $I\Delta C/CI$ tan δ $I_{lk20^{\circ}C}$	-55 °C/+125 °C/30 min ≤ 3 % of initial value ≤ initial limit value ≤ initial limit value

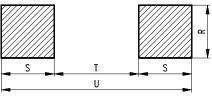
Measuring and test conditions only apply to B 45 194 if explicitly stated. Please contact your nearest Siemens Office for Passive Components if you need further information.

Soldering Conditions

1 Tests

Wetting	Solder	Bath temperature	Immersion time
in accordance with IEC 68-2-58	SnPb 60/40	235 (± 5) °C	2 (± 0,2) s
Preconditioning: Immersion in F-SW 31 flux	SnPb 60/40	215 (± 3) °C	3 (± 0,3) s
Assessment criterion: Wetting of terminals ≥ 95 % (except for cutting and bending edges)			
Resistance to soldering heat in accordance with IEC 68-2-58	Solder	Bath temperature	Immersion time
	SnPb 60/40	260 (± 5) °C	10 (± 0,5) s
Preconditioning: Immersion in F-SW 32 flux			
Assessment criterion	tan δ ≤ initi	o of initial value al limit value al limit value	
Resistance to soldering heat	Solder	Bath temperature	Immersion time
of B 45 194	SnPb 60/40	260 (± 5) °C	5 (± 1) s

2 Recommended solder pad layouts



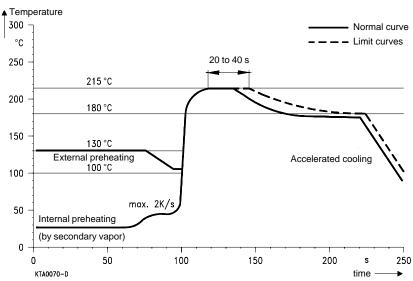
KTA0177-7

Case size	Soldering process	Dimensi			
		R	S	T	U
Z	Wave soldering	1,2	1,1	1,0	3,2
	Reflow soldering	1,2	0,8	1,0	2,6
Р	Wave soldering	1,6	2,6	1,3	6,5
	Reflow soldering	1,6	1,5	1,3	4,3
A	Wave soldering	1,6	1,9	1,2	5,0
	Reflow soldering	1,5	1,5	0,8	3,8
В	Wave soldering	2,7	2,0	1,5	5,5
	Reflow soldering	2,5	1,5	1,1	4,1
С	Wave soldering	2,7	2,8	3,0	8,6
	Reflow soldering	2,5	2,0	2,6	6,6
D	Wave soldering	2,9	2,9	4,4	10,2
	Reflow soldering	2,7	2,0	3,9	7,9
E	Wave soldering	2,9	2,9	4,4	10,2
	Reflow soldering	2,7	2,0	3,9	7,9

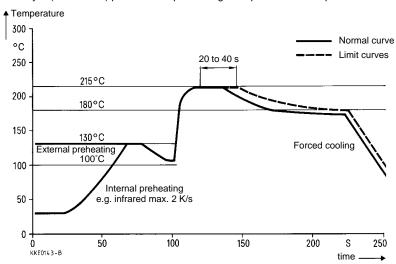
3 Recommended soldering temperature profiles for chip capacitors (in accordance with CECC 00802 Edition 2)

Vapor phase soldering

Chamber (batch) process with preheating. Temperature at component terminal applies

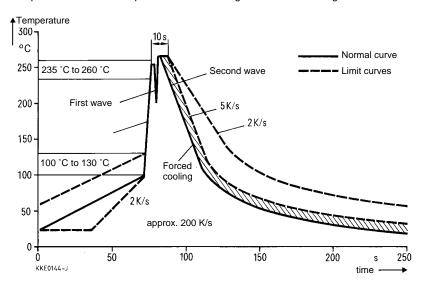


Conveyor (continuous) process with preheating. Temperature at component terminal applies



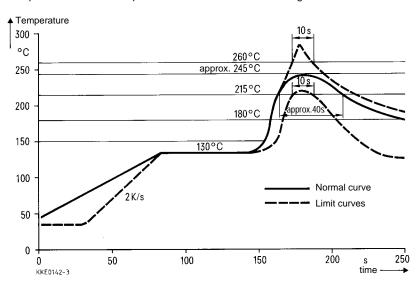
Wave soldering

Temperature curve at component terminal during dual wave soldering



Infrared reflow soldering

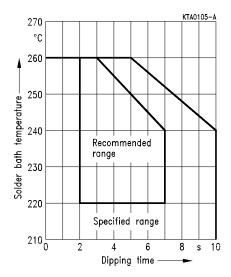
Temperature curve at component terminal in infrared soldering



Soldering Conditions

4 Recommended soldering temperature profiles for B 45 194

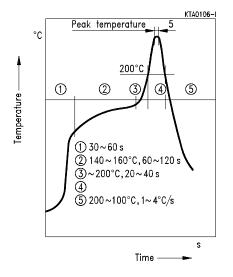
Wave soldering

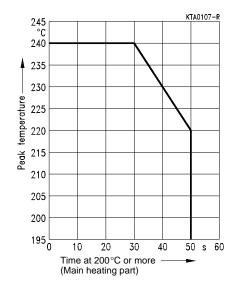


The components are fixed to the board with adhesive, and are directly dipped into the solder bath.

- When component mounting density is high, solderability may be decreased.
 Take notice to drain gas.
- Preheat for 2 minutes at 160 °C.
 Cool after soldering

Reflow soldering





- Temperature rising part I
 Ordinary temperature to preheating part
- ② Preheating part
- ③ Temperature rising part II Preheating part to 200 °C
- Main heating part Refer to figure on next page
- ⑤ Cooling part 200 to 100 °C

The components and the board are heated by an hot blast oven or an infrared radiation oven.

- Measure temperature at the component surface.
- Do not perform reflow more than twice.

Please consult supplier for vapor phase soldering conditions.



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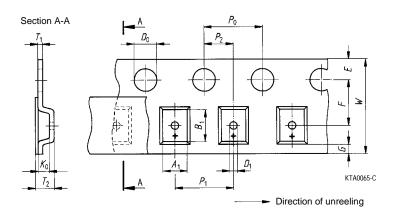


Taping, Packing and Weights

1 Taping

Chip capacitors are taped and reeled in accordance with IEC 286-3. Sizes Z, P, A and B are supplied in 8-mm blister tapes, sizes C, D and E in 12-mm blister tapes. The tapes and reels are antistatic. The position of the positive pole is shown in the outline drawing below.

Tape dimensions and tolerances

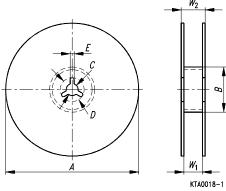


Dimensions	Case siz	ze					
(mm)	Z	Р	Α	В	С	D	Е
$A_1 \pm 0,2$	1,35	1,9	1,9	3,3	3,7	4,7	4,7
$B_1 \pm 0,2$	2,2	3,5	3,5	3,8	6,5	7,7	7,7
$D_0 + 0,1/-0$	1,5	1,5	1,5	1,5	1,5	1,5	1,5
D_1 min.	1,0	1,0	1,0	1,0	1,5	1,5	1,5
$P_0^{\cdot} \pm 0,1^{1}$	4,0	4,0	4,0	4,0	4,0	4,0	4,0
$P_1^{\circ} \pm 0,1$	4,0	4,0	4,0	4,0	8,0	8,0	8,0
$P_2^{\cdot} \pm 0.05$	2,0	2,0	2,0	2,0	2,0	2,0	2,0
W ±0,3	8,0	8,0	8,0	8,0	12,0	12,0	12,0
$E \pm 0,1$	1,75	1,75	1,75	1,75	1,75	1,75	1,75
$F \pm 0.05$	3,5	3,5	3,5	3,5	5,5	5,5	5,5
G min.	0,75	0,75	0,75	0,75	0,75	0,75	0,75
$T_1 \pm 0.05$	0,25	0,25	0,25	0,25	0,3	0,3	0,3
T_2 max.	1,9	1,9	2,3	2,6	3,3	3,6	4,8
$K_0^- \pm 0,1$	1,5	1,5	1,9	2,2	3,0	3,3	4,5

^{1) 0,2} mm over 10 sprocket hole spaces

Taping, Packing and Weights





Dime	ensions (mm)	Reel		
		180 mm diameter	330 mm diameter	
Α		180 ± 0,5	$330 \pm 0,5$	
В		62,5 – 2,5	62,0 ± 1,5	
C		13,0 ± 0,5	12,75 + 0,15/-0	
D		22,0 ± 1,0	21,0 ± 0,5	
Ε		$2,0 \pm 0,5$	2,0 + 0,5/-0	
$\overline{W_1}$	(8-mm tape)		8,4 + 0,2/-0	
	(12-mm tape)		12,4 + 0,2/-0	
$\overline{W_2}$	(8-mm tape)	11,0 ± 0,1	8,4 + 0,2/-0	
	(12-mm tape)	15,0 ± 0,1	12,4 + 0,2/-0	

3 Packing units and weights

Case size	Taped; pieces/reel	Taped; pieces/reel	
	180 mm diameter	330 mm diameter	g ¹⁾
Z	3000	_	0, 008
P	3000	_	0,02
A	2000	9000	0,06
В	2000	8000	0,09
С	750	3000	0,20
D	750	2800	0,35
E	400	1800	0,50

¹⁾ Guideline values, possible deviations of up to approximately $\pm~30~\%$

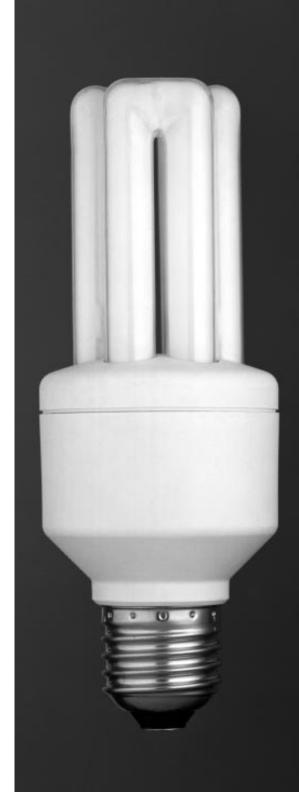


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Subject Index

Α		final inspection	64
ac power dissipation	52	fit FMEA	68
anode	43	FINEA	62
AQL figures	66	1	
В		IEC climatic category	58
back-to-back circuit	47	impedance	50
basic construction	43	incoming goods inspection	66
		incorrect polarity	47
C		inherent voltage	47 66
capacitance	48	inoperatives ISO 9000	61
frequency dependence	49	130 9000	01
temperature dependence	48	L	
capacitance tolerance	48	-	
category temperature	57	leakage current	55
cathode	43	after storage	57
CEDAC diagram	62	measurement of	56
charge-discharge proof	49	temperature dependence	55
circuit resistance	71	time dependence	56
claims and complaints	74	voltage dependence	55
cleaning agents	58	М	
D		material procurement	64
damp heat	75	maximum continuous voltage	45
damp heat conditions	57	maximum ripple current	52
delivery quality	66	mounting	58
dielectric	43	· ·	
disposal of old capacitors	60	N	
dissipation factor	54	non-conformancies	66
E		0	
EN ISO 9001	61	0	
end of use	60	operating voltage	46
endurance	75	ordering code	60
equivalent circuit diagram	50		
equivalent series resistance	50	Р	
F		packing	59, 84
-		packing units	84
failure criteria	68	part number	60
failure percentage	67	polarity	44
failure rate	68	polarity reversal voltage	47 53
calculation examples	72 70	power dissipation	53 64
conversion factors values	70 69	product monitoring product quality assurance	64
values	US	product quality assurance	04

Subject Index

Q		W	
quality assurance	61	weights	84
quality assurance procedure	64	_	
quality assurance procedures		Z	
chip capacitors	65	zero defects concept	62
quality assurance system	63		
quality data	73		
R			
random sampling	66		
rated capacitance	48		
rated voltage	45		
recharging	47		
reliability	68		
resistance to climatic stress	57		
S			
series back-to-back connection	47		
series resistance	71		
service life	67		
shock	75		
solder pad layouts	77		
soldering tests	76		
SPC	62		
standard barcode label	58		
standards	44		
statistical process control	64		
storage and transportation temperatures	58		
storage test	75		
superimposed alternating voltage	52		
surge voltage	46		
Т			
taping	83		
temperature coefficient, positive	48		
temperature range	57		
tolerance at delivery	48		
total quality management	62		
transient voltages	46		
V			
vibration	75		
voltages	45		

Symbols and Terms

Symbol	English	German
C C _R	Capacitance Rated capacitance	Kapazität Nennkapazität
ΔC $\Delta C/\Delta C_{R}$	Capacitance change Capacitance tolerance	Kapazitätsänderung Kapazitätstoleranz
$C_{\rm S}$	Series capacitance	Serienkapazität
C_{f}	Capacitance at frequency <i>f</i>	Kapazität bei Frequenz <i>f</i>
·		i i
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
ESR _T	Equivalent series resistance at temperature <i>T</i>	Ersatz-Serienwiderstand bei Temperatur T
	at temperature 1	bei remperatur /
f	Frequency	Frequenz
1	Current	Strom
lac	Alternating current	Wechselstrom
I_{f}	Alternating current at frequency f	Wechselstrom bei Frequenz f
I_{lk}	Leakage current	Reststrom
Р	Power dissipation	Verlustleistung
R _S	Series resistance (circuit resistance)	Serienwiderstand (Schaltkreiswiderstand)
Т	Temperature	Temperatur
T_{max}	Upper category temperature	Obere Grenztemperatur (Kategorietemperatur)
T_{min}	Lower category temperature	Untere Grenztemperatur (Kategorietemperatur)
T_{A}	Ambient temperature	Umgebungstemperatur
t	Time	Zeit
t_{op}	Operating time	Betriebszeit
V	Voltage	Spannung
Vac	AC voltage	Wechselspannung
$V_{\rm cont}$	Max. continuous voltage	Dauergrenzspannung
V_{F}	Forming voltage	Formierspannung
$V_{\sf op}$	Operating voltage	Betriebsspannung
V_{R}	Rated voltage	Nennspannung
V_{rev}	Reverse voltage	Umpolspannung
V_{S}	Surge voltage	Spitzenspannung
Z	Impedance	Scheinwiderstand
Z_{T}	Impedance at temperature T	Scheinwiderstand bei Temperatur T
tan δ	Dissipation factor	Verlustfaktor
$tan \; \delta_T$	Dissipation factor at temperature T	Verlustfaktor bei Temperatur T
tan δ_f	Dissipation factor at frequency f	Verlustfaktor bei Frequenz f

Symbols and Terms

Symbol	English	German
λ	Failure rate (1 fit = $1 \cdot 10^{-9}$ failures/h)	Ausfallrate (1 fit = 1 · 10 ⁻⁹ Ausfälle/h)
π_V π_T π_{Rs}	Factors for failure rate calculation: Factor for voltage dependence Factor for temperature dependence Factor for dependence on circuit resistance	Faktoren zur Berechnung der Ausfallrate: Faktor für Spannungsabhängigkeit Factor für Temperaturabhängigkeit Faktor für Abhängigkeit vom Schaltkreis- widerstand
	Decimal points	are indicated by commas.